PRIMA: Passive Reduced-Order Interconnect Macromodeling Algorithm

Altan Odabasioglu, Mustafa Celik, Member, IEEE, and Lawrence T. Pileggi, Senior Member, IEEE

Abstract— This paper describes an algorithm for generating provably passive reduced-order N-port models for RLC interconnect circuits. It is demonstrated that, in addition to macromodel stability, macromodel passivity is needed to guarantee the overall circuit stability once the active and passive driver/load models are connected. The approach proposed here, PRIMA, is a general method for obtaining passive reduced-order macromodels for linear RLC systems. In this paper, PRIMA is demonstrated in terms of a simple implementation which extends the block Arnoldi technique to include guaranteed passivity while providing superior accuracy. While the same passivity extension is not possible for MPVL, comparable accuracy in the frequency domain for all examples is observed.

I. INTRODUCTION

S integrated circuits and systems continue to be designed smaller and for faster operation, *RLC* interconnect effects have a more dominant impact on signal propagation than ever before. In addition, parasitic coupling effects and reduced power supply voltage levels make interconnect modeling increasingly important. Since these interconnect models can contain thousands of tightly coupled *R*–*L*–*C* components, reduced-order macromodels are imperative [1]–[4]. Ideally, a simulator would isolate the large linear portions of the circuit from the nonlinear elements (e.g., transistor models) and preprocess them into reduced order multiport macromodels (Fig. 1).

It is well known that an N port can be fully represented by its admittance parameters in the Laplace domain (Fig. 2); however, the objective is to apply model order reduction to produce low-order rational approximations for each entry in the admittance matrix, Y(s). A single-input single-output (SISO) N-port model approach would perform model order reduction on each term Y_{ij} individually. Both asymptotic waveform evaluation (AWE) [1] and Padé via Lanczos (PVL) [2], which are Padé approximations, can perform SISO reduction by matching 2q moments for a *q*th-order approximation of each Y_{ij} term. The Arnoldi algorithm [4] can also be used to obtain SISO approximations; however, it matches only *q* moments for a *q*th-order approximation. MPVL (matrix Padé

Manuscript received February 6, 1997. This work was supported in part by the Defense Advanced Research Projects Agency, sponsored by the Air Force Office of Scientific Research, under Grant F49620-96-1-0069, and by grants from Intel Corporation and SGS-Thomson Microelectronics. This paper was recommended by Associate Editor K. Mayaram.

A. Odabasioglu is with Monterey Design Systems, Sunnyvale, CA 94089 USA.

M. Celik is with Pacific Numerix Corporation, Scottsdale, AZ 85258 USA. L. T. Pileggi is with the Department of Electrical and Computer Engineer-

ing, Carnegie Mellon University, Pittsburgh, PA 15213 USA.

Publisher Item Identifier S 0278-0070(98)05822-9.

Linear elements

Fig. 1. Preprocessing of linear portion of circuit into N-port macromodels.



Fig. 2. Multiport representation of a linear circuit.

via Lanczos) [5] and block Arnoldi [6] are multi-input multioutput (MIMO) versions of PVL and Arnoldi, respectively. The projection perspective to the block Lanczos process has also been developed [7]. In the block techniques, the system modified nodal analysis (MNA) matrices are directly reduced by matrix transformations. Refer to [8] for a recent summary of useful order reduction methods.

In general, the reduced-order model of an *RLC* circuit can have unstable poles. Although it is always possible to obtain an asymptotically stable model by simply discarding the unstable poles, passivity is not guaranteed. The coordinate-transformed Arnoldi algorithm proposed in [9] was introduced as a remedy for the instability problem, but it cannot guarantee passivity. The PACT algorithm [3] proposed a new direction for the passive reduced-order model for *RC* circuits based on congruence transformations. More recently, the same authors proposed split congruence transformations [10] for passive reductions of *RLC* circuits, producing equivalent circuit realizations. In [10], however, the extra steps required to split the transformation matrix can result in a decrease in accuracy.

A passive system denotes a system that is incapable of generating energy, and hence one that can only absorb energy from the sources used to excite it [11]. Passivity is an important property to satisfy because stable, but not passive macromodels can produce unstable systems when connected to other stable, even passive, loads. A property in classical circuit theory states that: interconnections of stable systems may not necessarily be stable; but (strictly) passive circuits are (asymptotically) stable;



Fig. 3. Illustration of the formation of the MNA matrices for a two-port RLC circuit example.

and arbitrary interconnections of (strictly) passive circuits are (strictly) passive, and therefore, (asymptotically) stable [12].

In this paper, we propose a passive reduced-order interconnect macromodeling algorithm, PRIMA, based on the block Arnoldi algorithm, but with congruence transformations that produce provably passive reduced-order macromodels for arbitrary *RLC* circuits. PRIMA demonstrates accuracy comparable to MPVL and superior to block Arnoldi. The underlying technique for passive reduction, which is the basis for PRIMA, is also general enough to employ numerous Krylov space generation methods [7], [8], [13].

II. BACKGROUND

As shown in Fig. 3, voltage sources are connected to the ports to obtain the admittance matrix of a multiport. The N port, along with these sources, constitutes our time-domain modified nodal analysis (MNA) circuit equations:

$$C\dot{\boldsymbol{x}}_n = -\boldsymbol{G}\boldsymbol{x}_n + \boldsymbol{B}\boldsymbol{u}_N$$
$$\boldsymbol{i}_N = \boldsymbol{L}^T\boldsymbol{x}_n. \tag{1}$$

The i_N and u_N vectors denote the port currents and voltages, respectively, and

$$C \equiv \begin{bmatrix} \boldsymbol{Q} & \boldsymbol{0} \\ \boldsymbol{0} & \boldsymbol{H} \end{bmatrix} \quad \boldsymbol{G} \equiv \begin{bmatrix} \boldsymbol{N} & \boldsymbol{E} \\ -\boldsymbol{E}^T & \boldsymbol{0} \end{bmatrix} \quad \boldsymbol{x}_n \equiv \begin{bmatrix} \boldsymbol{v} \\ \boldsymbol{i} \end{bmatrix} \quad (2)$$

where v and i are the MNA variables [yielding a total number of n unknowns in (1)] corresponding to the node voltages and the branch currents for voltage sources and inductors, respectively. The matrices $G \in \Re^{n \times n}$ and $C \in \Re^{n \times n}$ represent the conductance and susceptance matrices (except that the rows corresponding to the current variables are negated as in [9]). N, Q, and H are the matrices containing the stamps for resistors, capacitors, and inductors, respectively. E consists of ones, minus ones, and zeros, which represent the current variables in KCL equations. Provided that the original N port is composed of passive linear elements only, Q, H, and N are symmetric nonnegative definite matrices. It is straightforward to show that C is a symmetric nonnegative definite matrix with this MNA formulation.

An MNA formulation example for a small circuit is shown in Fig. 3. Since this is an *N*-port formulation, whereby the only sources are the voltage sources at the *N*-port nodes, B = L where $B \in \Re^{n \times N}$. But we will maintain the separate *B* and *L* notation throughout this paper for equation generality.

Assuming for now that we are interested in admittance parameters and returning to (1), following the notation in [2], we define

$$A \equiv -G^{-1}C \quad \text{and} \quad R \equiv G^{-1}B. \tag{3}$$

With unit impulse voltages at the ports, taking the Laplace transformation of (1) and solving for the port current variables, the *y*-parameter matrix is given as

$$Y(s) = L^T (G + sC)^{-1} B.$$
(4)

Using (3), the admittance matrix can also be expressed as

$$\boldsymbol{Y}(s) = \boldsymbol{L}^T (\boldsymbol{I}_n - s\boldsymbol{A})^{-1} \boldsymbol{R}$$
⁽⁵⁾

where I_n is the $n \times n$ identity matrix. It is apparent from (5) that the eigenvalues of A represent the reciprocal poles of Y(s). We can define the impedance parameter matrix Z(s) in a similar way. Specifically, with the unit impulse current sources connected to the N port, the port voltages would represent the z-parameter terms.

Considering either the admittance or impedance representation, we define the block moments and the block Krylov space as follows for this paper. *Definition 1:* The *block moments* of Y(s) are defined as the coefficients of Taylor expansion of Y(s) around s = 0:

$$\boldsymbol{Y}(s) = \boldsymbol{M}_0 + \boldsymbol{M}_1 s + \boldsymbol{M}_2 s^2 + \cdots \tag{6}$$

where $M_i \in \Re^{N \times N}$. These block moments can be computed using the relation

$$\boldsymbol{M}_i = \boldsymbol{L}^T \boldsymbol{A}^i \boldsymbol{R}. \tag{7}$$

In circuit terms, the entry in the *j*th row, *k*th column of M_i is the *i*th moment of the current that flows into port *j* when the voltage source at port *k* is the only nonzero source.

Definition 2: The block Krylov space generated by matrices $A \in \Re^{n \times n}$ and $R = [r_0 r_1 \cdots r_N] \in \Re^{n \times N}$ are defined as¹

$$Kr(\boldsymbol{A}, \boldsymbol{R}, q) \equiv colsp[\boldsymbol{R}, \boldsymbol{A}\boldsymbol{R}, \boldsymbol{A}^{2}\boldsymbol{R}, \cdots, \boldsymbol{A}^{k-1}\boldsymbol{R}, \boldsymbol{A}^{k}\boldsymbol{r}_{0}, \boldsymbol{A}^{k}\boldsymbol{r}_{1}, \cdots, \boldsymbol{A}^{k}\boldsymbol{r}_{l}]$$
$$k = \lfloor q/N \rfloor, \quad l = q - kN. \quad (8)$$

From the circuit's perspective, the vector $A^j r_k$ contains the set of *j*th moments for the MNA variables [*x* in (2)] when the *k*th source is active and all other sources in the circuit are set to be zero. In matrix terms, the block Krylov space spans the combination of moment vectors generated by different sources in the circuit.

A. Block Arnoldi Algorithm

The block Arnoldi algorithm reduces the system matrix Ain (3) to a small block upper Hessenberg matrix H_q . The algorithm involves successively filling in the columns of Xin the relation $AX = XH_q$ subject to $X^TX = I_q$. Here, $X \in \Re^{n \times q}$ is an orthonormal matrix spanning the Krylov space $Kr(A, R, q), H_q \in \Re^{q \times q}$ is a block upper Hessenberg matrix,² and $I_q \in \Re^{q \times q}$ is an identity matrix. In summary

$$colsp(\mathbf{X}) = Kr(\mathbf{A}, \mathbf{R}, q)$$
$$\mathbf{X}^{T}\mathbf{A}\mathbf{X} = \mathbf{H}_{q}$$
$$\mathbf{X}^{T}\mathbf{X} = \mathbf{I}_{q}.$$
(9)

Finding the reduced-order admittance matrix can be explained by a change of variable in (1):

$$\boldsymbol{x}_n = \boldsymbol{X} \boldsymbol{z}_q \tag{10}$$

where $z_q \in \Re^{q \times 1}$ is now the reduced-order system variable. This reduces the number of unknowns in the system since q is generally much smaller than n. Substituting (10) into (1), then premultiplying first by G^{-1} and then by X^T yields

$$-X^{T}G^{-1}CX\dot{z}_{q} = X^{T}Xz_{q} - X^{T}G^{-1}Bu_{N}$$
$$i_{N} = L^{T}Xz_{q}.$$
(11)

Recalling (3) and using the relations in (9) gives

$$H_q \dot{z}_q = z_q - X^T R u_N$$

$$i_N = L^T X z_q.$$
(12)

¹The $\lfloor \cdot \rfloor$ operator is the truncation to the nearest integer toward zero.

²A matrix **H** is an upper Hessenberg matrix if $H_{ij} = 0$ when i > j + 1.

Therefore, in the Laplace domain

$$\hat{\boldsymbol{Y}}(s) = \boldsymbol{L}^T \boldsymbol{X} (\boldsymbol{I}_q - s \boldsymbol{H}_q)^{-1} \boldsymbol{X}^T \boldsymbol{R}.$$
(13)

The reduced-order system equations and admittance matrix are given by (12) and (13), respectively. The poles of the reduced-order system are the reciprocal eigenvalues of H_q . A complete pole/residue decomposition can be obtained by eigendecomposing H_q

$$\boldsymbol{H}_q = \boldsymbol{S} \boldsymbol{\Lambda}_q \boldsymbol{S}^{-1} \tag{14}$$

$$\hat{Y}(s) = L^T X S (I_q - s\Lambda_q)^{-1} S^{-1} X^T R.$$
(15)

The inversion of $(I_q - s\Lambda_q)$ is trivial because it is a diagonal matrix.

In [6], it is shown that

$$\boldsymbol{A}^{i}\boldsymbol{R} = \boldsymbol{X}\boldsymbol{H}_{q}^{i}\boldsymbol{X}^{T}\boldsymbol{R}, \qquad 0 \leq i < \left\lfloor \frac{q}{N} \right\rfloor$$
(16)

from which one can derive that the first $\lfloor q/N \rfloor$ block moments of $\hat{Y}(s)$ in (13) and Y(s) in (5) match. In fact, it can be shown that if X spans the *k*th moment vector that is generated by the *j*th source, then the *k*th columns of the *j*th block moments of $\hat{Y}(s)$ and Y(s) match.

The accuracy of the block Arnoldi approximation gradually increases as the order is increased since more moments of the original admittance matrix will be matched. Since it does not directly use moments, the block Arnoldi algorithm does not suffer from the same numerical inaccuracy as AWE. The algorithm requires one LU decomposition of G and q backward–forward substitutions to generate the block Krylov space.

III. PRIMA: PASSIVE REDUCED-ORDER INTERCONNECT MACROMODELING ALGORITHM

In this section, we present our passive reduced-order macromodeling algorithm. The algorithm given here is based on the block Arnoldi algorithm. However, it should be noted that PRIMA is a general technique for the passive reduction of *RLC* circuits, and is not bound to a particular Arnoldi or Lanczos process. The PRIMA algorithm, as applied with the block Arnoldi algorithm, is summarized in Fig. 4.

After $\lfloor q/N \rfloor + 1$ (the extra step is not necessary when (q/N) is an integer) iterations of the block Arnoldi algorithm, the $n \times q$ matrix \boldsymbol{X} and a $q \times q$ upper Hessenberg matrix \boldsymbol{H} are found such that

$$colsp(\mathbf{X}) = Kr(\mathbf{A}, \mathbf{R}, q)$$
$$\mathbf{X}^{T}\mathbf{X} = \mathbf{I}_{q}$$
$$\mathbf{X}^{T}\mathbf{A}\mathbf{X} = \mathbf{H}.$$
(17)

In the classical Arnoldi approach described in Section II-A and as employed in [4], the reduced-order Y(s) is given as

$$\hat{\boldsymbol{Y}}(s) = \boldsymbol{L}^T \boldsymbol{X} (\boldsymbol{I} - s\boldsymbol{H})^{-1} \boldsymbol{X}^T \boldsymbol{R}.$$
(18)

In the PRIMA algorithm, the conductance and susceptance matrices are directly reduced so that passivity is preserved OConnect voltage sources to the multiport & obtain the MNA matrices as in (2). OSet $[\mathbf{b}_1 | \mathbf{b}_2 | ... | \mathbf{b}_N] = \mathbf{B}$ and $[\mathbf{1}_1 | \mathbf{1}_2 | ... | \mathbf{1}_N] = \mathbf{L}$

OSolve
$$\mathbf{GR} = \mathbf{B}$$
 for \mathbf{R}

Set V = CV

 $O(\mathbf{X}_0, \mathbf{T}) = qr(\mathbf{R})$; qr factorization of **R**

OIF
$$\frac{q}{N}$$
 is not an integer, set $n = \lfloor \frac{q}{N} \rfloor + 1$, else set $n = \frac{q}{N}$

OFor *k*=1, 2, ..., *n*

Solve
$$\mathbf{G} \mathbf{X}_{k}^{(0)} = \mathbf{V}$$
 for $\mathbf{X}_{k}^{(0)}$
For $j=1, ..., k$
 $\mathbf{H} = \mathbf{X}_{k-j}^{T} \mathbf{X}_{k}^{(j-1)}$
 $\mathbf{X}_{k}^{(j)} = \mathbf{X}_{k}^{(j-1)} - \mathbf{X}_{k-j} \mathbf{H}$
 $(\mathbf{X}_{k}, \mathbf{T}) = qr(\mathbf{X}_{k}^{(k)})$; qr factorization of $\mathbf{X}_{k}^{(k)}$

OSet $\mathbf{X} = \begin{bmatrix} \mathbf{X}_0 \ \mathbf{X}_1 \ \dots \ \mathbf{X}_{k-1} \end{bmatrix}$ and truncate \mathbf{X} so that it has q columns only.

OCompute $\tilde{\mathbf{C}} = \mathbf{X}^T \mathbf{C} \mathbf{X}, \ \tilde{\mathbf{G}} = \mathbf{X}^T \mathbf{G} \mathbf{X}$

ORealize the macromodel as outlined in Section 4.1 or

OFind eigendecomposition of $\tilde{\mathbf{G}}^{-1}\tilde{\mathbf{C}}$: $\tilde{\mathbf{G}}^{-1}\tilde{\mathbf{C}} = \mathbf{S} \wedge \mathbf{S}^{-1}$ $\bigcirc \Lambda = diag(\lambda_1, \lambda_2, ..., \lambda_a)$

OTo find poles and residues for $Y_{i,j}(s)$:

Solve
$$\tilde{\mathbf{G}} \mathbf{w} = \mathbf{X}^T \mathbf{b}_j$$
 for \mathbf{w}
Set $\mu = \mathbf{S}^T \mathbf{X}^T \mathbf{l}_i$ and $\mathbf{v} = \mathbf{S}^{-1} \mathbf{w}$
 $Y_{i, j}(s) = \sum_{n=1}^{q} \frac{\mu_n \mathbf{v}_n}{1 - s\lambda_n}$
mpute all $Y_{i, j}(s)$ to find $\mathbf{Y}(s) = \begin{bmatrix} Y_{1, 1} \dots Y_{1, N} \\ \vdots \\ Y_{N, 1} \dots Y_{N, N} \end{bmatrix}$

Fig. 4. Simple PRIMA implementation.

OCor

during reduction. Applying the change of variable $x_n = X\tilde{x}_q$ in (1), and multiplying the first row by X^T from (17) yields

$$(X^T C X) \dot{\check{x}}_q = -(X^T G X) \dot{\check{x}}_q + (X^T B) u_N$$

 $i_N = (L^T X) \dot{\check{x}}_q.$ (19)

So, for the macromodel, the reduced-order MNA matrices are

$$\tilde{C} = \mathbf{X}^T C \mathbf{X} \quad \tilde{G} = \mathbf{X}^T G \mathbf{X}
\tilde{B} = \mathbf{X}^T B \quad \tilde{L} = \mathbf{X}^T L.$$
(20)

These types of transformations are known as congruence transformations. Congruence transformations were first introduced by [3] for order reduction of circuits. From (19) and (20), the reduced Y(s), namely, $\hat{Y}(s)$, is now

$$\hat{\boldsymbol{Y}}(s) = \tilde{\boldsymbol{L}}^T \left(\tilde{\boldsymbol{G}} + s \tilde{\boldsymbol{C}} \right)^{-1} \tilde{\boldsymbol{B}}.$$
(21)

Since the size of \tilde{G} and \tilde{C} is typically very small, it is easy to find the poles and zeros of $\hat{Y}(s)$ by eigendecomposition. The complexity of the algorithm is basically equivalent to that of the block Arnoldi process. As noticed, the block Arnoldi algorithm is used only as a means to generate the block Krylov space X that is used for the congruence transformations. We will further explore the connections between PRIMA and block Arnoldi in Section III-C.

A. Preservation of Passivity

If the system described by (1) and (2) is reduced by the transformations in (20), it can be shown that the reduced system is always passive. In [14], necessary and sufficient conditions for the system admittance matrix $\hat{Y}(s) = \tilde{L}^T (\tilde{G} + s\tilde{C})^{-1}\tilde{B}$ to be passive are the following.

- 1) $\hat{Y}(s^*) = \hat{Y}^*(s)$ for all complex s, where * is the complex conjugate operator.
- 2) $\hat{Y}(s)$ is a positive matrix, that is, $z^{*T}(\hat{Y}(s) + \hat{Y}^{T}(s^{*}))z \ge 0$ for all complex values of s satisfying $\operatorname{Re}(s) > 0$ and for any complex vector z.

The second condition also implies the analyticity of $\hat{Y}(s)$ for $\operatorname{Re}(s) > 0$ since $\hat{Y}(s)$ is a rational function of s (details in [14]). Therefore, the test of analyticity is unnecessary.

Due to the fact that the reduced matrices $\tilde{G}, \tilde{C}, \tilde{B}$, and \tilde{L} are all real since the transformation matrix X is real, condition 1) is automatically satisfied. To show that condition 2) is satisfied, we first set $Y_h(s) = \hat{Y}(s) + \hat{Y}^T(s^*)$ and use the property $\tilde{B} = \tilde{L}$ (since B = L in our formulation, $X^T B = X^T L$) and some algebra to obtain

$$z^{*T}Y_{h}(s)z$$

$$= z^{*T} \Big(\tilde{B}^{T} \Big(\tilde{G} + s\tilde{C} \Big)^{-1} \tilde{B} + \tilde{B}^{T} \Big(\tilde{G} + s^{*}\tilde{C} \Big)^{-T} \tilde{B} \Big) z$$

$$= z^{*T} \tilde{B}^{T} \Big[\Big(\tilde{G} + s\tilde{C} \Big)^{-1} + \Big(\tilde{G} + s^{*}\tilde{C} \Big)^{-T} \Big] \tilde{B}z$$

$$= z^{*T} \tilde{B}^{T} \Big(\tilde{G} + s\tilde{C} \Big)^{-1} \Big[\Big(\tilde{G} + s\tilde{C} \Big) + \Big(\tilde{G} + s^{*}\tilde{C} \Big)^{T} \Big]$$

$$\times \Big(\tilde{G} + s^{*}\tilde{C} \Big)^{-T} \tilde{B}z. \qquad (22)$$

Setting $\boldsymbol{w} = (\tilde{\boldsymbol{G}} + s^* \tilde{\boldsymbol{C}})^{-T} \tilde{\boldsymbol{B}} \boldsymbol{z}$ and $s = j\omega + \sigma$ yields

$$\begin{aligned} \boldsymbol{z}^{*T} \boldsymbol{Y}_{h}(s) \boldsymbol{z} \\ &= \boldsymbol{w}^{*T} \bigg[\left(\tilde{\boldsymbol{G}} + (\sigma + j\omega) \tilde{\boldsymbol{C}} \right) + \left(\tilde{\boldsymbol{G}} + (\sigma - j\omega) \tilde{\boldsymbol{C}} \right)^{T} \bigg] \boldsymbol{w} \\ &= \boldsymbol{w}^{*T} \bigg[\tilde{\boldsymbol{G}} + \tilde{\boldsymbol{G}}^{T} + \sigma \bigg(\tilde{\boldsymbol{C}} + \tilde{\boldsymbol{C}}^{T} \bigg) \bigg] \boldsymbol{w} \\ &= \boldsymbol{w}^{*T} \big[\boldsymbol{X}^{T} \boldsymbol{G} \boldsymbol{X} + \boldsymbol{X}^{T} \boldsymbol{G}^{T} \boldsymbol{X} + \sigma \big(\boldsymbol{X}^{T} \boldsymbol{C} \boldsymbol{X} + \boldsymbol{X}^{T} \boldsymbol{C}^{T} \boldsymbol{X} \big) \big] \boldsymbol{w} \\ &= \boldsymbol{w}^{*T} \boldsymbol{X}^{T} \big[\boldsymbol{G} + \boldsymbol{G}^{T} + \sigma \big(\boldsymbol{C} + \boldsymbol{C}^{T} \big) \big] \boldsymbol{X} \boldsymbol{w}. \end{aligned}$$
(23)

Similarly, let $\boldsymbol{y} = \boldsymbol{X}\boldsymbol{w}$ to get

$$\boldsymbol{z}^{*T}\boldsymbol{Y}_{h}(s)\boldsymbol{z} = \boldsymbol{y}^{*T} \big[\boldsymbol{G} + \boldsymbol{G}^{T} + \sigma \big(\boldsymbol{C} + \boldsymbol{C}^{T} \big) \big] \boldsymbol{y}.$$
(24)

Since C is symmetric, $C^T + C = 2C$. C is known to be nonnegative definite [since we negate the rows corresponding to current variables as in (2)] so

$$\boldsymbol{y}^{*T}\sigma(\boldsymbol{C}^{T}+\boldsymbol{C})\boldsymbol{y}=2\sigma\boldsymbol{y}^{*T}\boldsymbol{C}\boldsymbol{y}\geq0$$
(25)

for any complex vector \boldsymbol{y} and $\sigma = \operatorname{Re}(s) > 0$. \boldsymbol{N} (the resistor stamps) is a symmetric nonnegative definite matrix; therefore

$$\boldsymbol{y}^{*T}(\boldsymbol{G}^{T} + \boldsymbol{G})\boldsymbol{y} = \boldsymbol{y}^{*T} \left(\begin{bmatrix} \boldsymbol{N} & \boldsymbol{E} \\ -\boldsymbol{E}^{T} & \boldsymbol{0} \end{bmatrix}^{T} + \begin{bmatrix} \boldsymbol{N} & \boldsymbol{E} \\ -\boldsymbol{E}^{T} & \boldsymbol{0} \end{bmatrix} \right) \boldsymbol{y}$$
$$= \boldsymbol{y}^{*T} \begin{bmatrix} 2\boldsymbol{N} & \boldsymbol{0} \\ \boldsymbol{0} & \boldsymbol{0} \end{bmatrix} \boldsymbol{y} \ge 0$$
(26)

is also nonnegative definite for any complex vector y. From (24)–(26), it follows that the second passivity condition is satisfied. The reader should note that in this proof, no assumptions on the transformation matrix X had been made for none was needed. This gives the algorithm significant flexibility in choosing X to improve macromodel accuracy and/or run time.

B. Preservation of Moments

In this section, it will be shown that the transformation in (20) preserves $\lfloor q/N \rfloor$ block moments of the original system, which is the same as the classical block Arnoldi reduction and half of that in MPVL. The block moments M_i of the original system, as shown in (7), are

$$\boldsymbol{M}_i = \boldsymbol{L}^T \boldsymbol{A}^i \boldsymbol{R} \tag{27}$$

where $A \equiv -G^{-1}C$, $R \equiv G^{-1}B$, and G, C, B, L are the system matrices as defined in (1).

Likewise, the moments of the reduced-order system are given by

$$\hat{\boldsymbol{M}}_{i} = \tilde{\boldsymbol{L}}^{T} \tilde{\boldsymbol{A}}^{i} \tilde{\boldsymbol{R}}$$
(28)

where $\tilde{A} \equiv -\tilde{G}^{-1}\tilde{C}$, $\tilde{R} \equiv \tilde{G}^{-1}\tilde{B}$, and \tilde{G} , \tilde{C} , \tilde{B} , \tilde{L} are as defined in (20). Substitution of (20) in (28) yields

$$\hat{\boldsymbol{M}}_{i} = \boldsymbol{L}^{T} \boldsymbol{X} \Big[- \big(\boldsymbol{X}^{T} \boldsymbol{G} \boldsymbol{X} \big)^{-1} \big(\boldsymbol{X}^{T} \boldsymbol{C} \boldsymbol{X} \big) \Big]^{i} \big(\boldsymbol{X}^{T} \boldsymbol{G} \boldsymbol{X} \big)^{-1} \boldsymbol{X}^{T} \boldsymbol{B}.$$
(29)

For a *q*th-order approximation, the columns of X span Kr(A, R, q); therefore, it is shown in [6] that

$$\boldsymbol{A}^{i}\boldsymbol{R} = \boldsymbol{X}\boldsymbol{H}_{q}^{i}\boldsymbol{X}^{T}\boldsymbol{R}, \qquad 0 \leq i < \left\lfloor \frac{q}{N} \right\rfloor. \tag{30}$$

Rearranging the terms and using the definitions from (20)

$$AA^{i-1}R = XH^{i}_{q}X^{T}R$$

$$-G^{-1}CA^{i-1}R = XH^{i}_{q}X^{T}R$$

$$-CA^{i-1}R = GXH^{i}_{q}X^{T}R$$

$$-X^{T}CA^{i-1}R = X^{T}GXH^{i}_{q}X^{T}R$$

$$-X(X^{T}GX)^{-1}X^{T}CA^{i-1}R = XH^{i}_{q}X^{T}R.$$
(32)

Inserting (30) in (32) results in

$$\boldsymbol{K}\boldsymbol{A}^{i-1}\boldsymbol{R} = \boldsymbol{A}^{i}\boldsymbol{R}, \qquad 0 \le i < \left\lfloor \frac{q}{N} \right\rfloor$$
(33)

where

$$\boldsymbol{K} = -\boldsymbol{X} \left(\boldsymbol{X}^T \boldsymbol{G} \boldsymbol{X} \right)^{-1} \boldsymbol{X}^T \boldsymbol{C}.$$
 (34)

From (33), it can be shown by recursion that

$$\boldsymbol{K}^{i}\boldsymbol{R} = \boldsymbol{A}^{i}\boldsymbol{R}, \qquad 0 \leq i < \left\lfloor \frac{q}{N} \right\rfloor.$$
 (35)

Therefore, using (34), it follows that

$$\boldsymbol{X}\left[-\left(\boldsymbol{X}^{T}\boldsymbol{G}\boldsymbol{X}\right)^{-1}\left(\boldsymbol{X}^{T}\boldsymbol{C}\boldsymbol{X}\right)\right]^{i}=\boldsymbol{K}^{i}\boldsymbol{X}.$$
(36)

Replacing $X[-(X^TGX)^{-1}(X^TCX)]^i$ in (29) with K^iX yields

$$\hat{\boldsymbol{M}}_{i} = \boldsymbol{L}^{T} \boldsymbol{K}^{i} \boldsymbol{X} \left(\boldsymbol{X}^{T} \boldsymbol{G} \boldsymbol{X} \right)^{-1} \boldsymbol{X}^{T} \boldsymbol{B}.$$
(37)

Evaluating (30) when i = 0 yields

$$\boldsymbol{R} = \boldsymbol{X}\boldsymbol{X}^T\boldsymbol{R}.$$
 (38)

Multiplying both sides by $X^T G$ gives

$$\boldsymbol{X}^T \boldsymbol{B} = \left(\boldsymbol{X}^T \boldsymbol{G} \boldsymbol{X} \right) \boldsymbol{X}^T \boldsymbol{R}$$
(39)

and it follows as

$$\boldsymbol{X} \left(\boldsymbol{X}^T \boldsymbol{G} \boldsymbol{X} \right)^{-1} \boldsymbol{X}^T \boldsymbol{B} = \boldsymbol{R}.$$
 (40)

Then, combining (37) and (40)

$$\hat{\boldsymbol{M}}_i = \boldsymbol{L}^T \boldsymbol{K}^i \boldsymbol{R}, \qquad 0 \le i < \left\lfloor \frac{q}{N} \right\rfloor.$$
 (41)

Finally, comparing (35) and (41) with (27), it follows that

$$\hat{\boldsymbol{M}}_i = \boldsymbol{M}_i, \qquad 0 \le i < \left\lfloor \frac{q}{N} \right\rfloor.$$
 (42)

Note that the number of poles in each entry of Y(s) is q, and we have matched the first $\lfloor q/N \rfloor$ moments at all N ports, yielding a total of q moments. In fact, it can be easily shown that if X spans the kth moment vector that is generated by the *j*th source, then the kth columns of the *j*th block moments of $\hat{Y}(s)$ and Y(s) match as in block Arnoldi. The number of moments matched in this particular implementation of PRIMA is, therefore, the same as that for the block Arnoldi algorithm and half as many as matched by the block Lanczos algorithm.

C. Connection to Block Arnoldi

The block-Arnoldi-based PRIMA produces a reduced-order model that is quite similar to that of the block Arnoldi process, but slightly different in ways which seem to improve the accuracy. We can express the PRIMA reduced-order system as in these system equations:

$$-\tilde{\boldsymbol{G}}^{-1}\tilde{\boldsymbol{C}}\dot{\tilde{\boldsymbol{x}}}_{q} = \tilde{\boldsymbol{x}}_{q} - \tilde{\boldsymbol{G}}^{-1}\tilde{\boldsymbol{B}}\boldsymbol{u}_{N}$$
$$\boldsymbol{i}_{N} = \tilde{\boldsymbol{L}}^{T}\tilde{\boldsymbol{x}}_{q}.$$
(43)

The poles of the reduced-order system in the block Arnoldi process are the inverses of the eigenvalues of H_q from (9). In PRIMA, however, the poles are the inverses of eigenvalues of $-\tilde{G}^{-1}\tilde{C}$. In this section, we show that the matrices H_q and $-\tilde{G}^{-1}\tilde{C}$ are identical, except for the last N columns (N is the number of ports) and the relation $\tilde{G}^{-1}\tilde{B} = X^T R$. Multiplying both sides with X^T in (30) gives

$$\boldsymbol{X}^{T}\boldsymbol{A}^{i}\boldsymbol{R} = \boldsymbol{H}_{q}^{i}\boldsymbol{X}^{T}\boldsymbol{R}, \qquad 0 \leq i < \left\lfloor \frac{q}{N} \right\rfloor.$$
(44)

Inserting (35) into (44) yields

$$\boldsymbol{X}^{T}\boldsymbol{K}^{i}\boldsymbol{R} = \boldsymbol{H}_{q}^{i}\boldsymbol{X}^{T}\boldsymbol{R}, \qquad 0 \leq i < \left\lfloor \frac{q}{N} \right\rfloor.$$
(45)

Inserting (38) into (45) results in

$$\boldsymbol{X}^{T}\boldsymbol{K}^{i}\boldsymbol{X}\boldsymbol{X}^{T}\boldsymbol{R} = \boldsymbol{H}_{q}^{i}\boldsymbol{X}^{T}\boldsymbol{R}, \qquad 0 \leq i < \left\lfloor \frac{q}{N} \right\rfloor.$$
(46)

We know from (36) that

$$\boldsymbol{X}^{T}\boldsymbol{K}^{i}\boldsymbol{X} = \left[-\left(\boldsymbol{X}^{T}\boldsymbol{G}\boldsymbol{X}\right)^{-1}\boldsymbol{X}^{T}\boldsymbol{C}\boldsymbol{X}\right]^{i} = \left[\tilde{\boldsymbol{G}}^{-1}\tilde{\boldsymbol{C}}\right]^{i}.$$
 (47)

Using (47) in (46) gives

$$\left[-\tilde{\boldsymbol{G}}^{-1}\tilde{\boldsymbol{C}}\right]^{i}\boldsymbol{X}^{T}\boldsymbol{R} = \boldsymbol{H}_{q}^{i}\boldsymbol{X}^{T}\boldsymbol{R}, \qquad 0 \leq i < \left\lfloor\frac{q}{N}\right\rfloor. \tag{48}$$

Since X is obtained via a block Arnoldi process, $X^T R$ is only nonzero in the first N rows since the other rows of X^T are built to be orthonormal to R:

$$\boldsymbol{X}^{T}\boldsymbol{R} = \begin{bmatrix} \boldsymbol{T}_{N\times N}^{T} & \boldsymbol{0}_{N\times N} & \boldsymbol{0}_{N\times N} & \cdots \end{bmatrix}^{T}$$
(49)

where T is the $N \times N$ upper triangular full rank matrix produced in the fourth step of the PRIMA algorithm given in Fig. 4. Therefore, left multiplication of any matrix by $X^T R$ only extracts the information about the first N columns of the matrix.

Hence, evaluating (48) when i = 1 means that the first N columns of H_q and $-\tilde{G}^{-1}\tilde{C}$ are the same. Since H_q is an upper block Hessenberg matrix (with blocks of $N \times N$), the first N columns of H_q^i (for i > 1) depend only on the first (i-1)N columns of H_q . From this information, evaluating (48) from i = 1 to $\lfloor q/N \rfloor - 1$ recursively demonstrates that the columns of $-\tilde{G}^{-1}\tilde{C}$ and H_q are the same until the last N columns.

To demonstrate that $\tilde{G}^{-1}\tilde{B} = X^T R$, we can simply multiply both sides of (40) by X^T and use the relation $X^T X = I_q$.

IV. TIME-DOMAIN SIMULATION OF THE MACROMODELS

For a complete simulation, the nonlinear elements should be simulated along with the reduced-order macromodels. The popular and reliable simulation tool for general nonlinear circuits is SPICE [15]. Here, we describe two ways to include the PRIMA macromodels into circuit simulators such as SPICE. The first technique is direct stamping of the reduced-order matrices into the general SPICE MNA matrix, whereas the second method is based on a *y*-parameter description of the macromodel. The first method can also lead to an equivalent SPICE netlist production as a result of the reduction.

A. Direct Stamping and Realization

Since the reduced-order circuit is described in real matrices, it can be directly stamped into the SPICE MNA matrices. Noticing that the reduced-order q-variable system has the governing equations as in (43), and recognizing that it is possible to introduce \tilde{x}_q as a circuit variable into the MNA matrix, the direct stamps for the macromodel can be generated as

$$\begin{bmatrix} \text{Stamps for } \mathbf{0} & \mathbf{0} \\ f(x_{NL}, u_p) & \mathbf{I}_N & \mathbf{0} \\ \mathbf{0} & \mathbf{0} & \mathbf{I}_N & -\tilde{\mathbf{L}}^T \\ \mathbf{0} & (\tilde{\mathbf{G}}^{-1}\tilde{\mathbf{B}}) & \mathbf{0} & (\mathbf{I} + \tilde{\mathbf{G}}^{-1}\tilde{\mathbf{C}}\frac{d}{dt}) \end{bmatrix} \begin{bmatrix} \mathbf{x}_{NL} \\ u_p \\ \mathbf{i}_p \\ \tilde{\mathbf{x}}_q \end{bmatrix}$$
$$= \begin{bmatrix} \mathbf{v}_{NL} \\ \mathbf{v}_p \\ \mathbf{0} \\ \mathbf{0} \end{bmatrix}.$$
(50)

In (50), \boldsymbol{x}_{NL} denotes the other variables of the circuit (other node voltages and currents), \boldsymbol{u}_p and \boldsymbol{i}_p are port voltages and currents, respectively, and $\tilde{\boldsymbol{x}}_q$ denotes the extra variables that are introduced from the inclusion of the realized macromodel into the circuit. The realized macromodel will introduce only q + N unknowns to the SPICE MNA matrix.

At this point, we understand it is possible to postprocess the system in (43) to achieve superior performance or an improved implementation. Without postprocessing, the $\tilde{G}^{-1}\tilde{C}$ matrix will be block upper Hessenberg, which degrades the sparsity of the SPICE MNA matrix, particularly when N is large.

B. Y-Parameter-Based Simulation

In order to compute the y parameters of the reduced-order system, the eigendecomposition steps [(14) and (15)] are used. After finding the poles and residues for Y(s), convolution is needed since the finite-difference methods employed in SPICE are for time-domain analysis and the macromodel is described by its y parameters in the frequency domain. Specifically, the currents at the ports would be computed by

$$i_p(t) = \sum_{j=1}^N \int_0^t y_{pj}(t-\tau) v_j(\tau) \, d\tau, \qquad 1 \le p \le N$$
 (51)

which requires $O(T^2)$ complexity, where T is the number of time points during simulation. For this reason, recursive convolution [16] and time-domain y-parameter macromodels [17] were developed, where the complexity is linear with the number of time points. Details of these approaches are available in [17] and [16].

V. RESULTS

In this section, our passive reduction algorithm is demonstrated and compared with other approaches. For the frequency-domain examples, the y parameters are compared with the reduced-order models from different reduction methods. Time-domain results are obtained using a modified version of SPICE3f4 [18] to perform recursive convolution. For all of the examples, the poles obtained via PRIMA were



Fig. 5. Stable but nonpassive macromodel.



Fig. 6. Waveform comparisons for a four port.

observed to be *stable* at all times, which was a practical verification for the analyticity condition of $\hat{Y}(s)$.

A. Nonlinear Driver Driving a Transmission Line

To demonstrate the importance of passivity, we considered the analysis of a lossy transmission line which was modeled by 40 lumped *RLC* sections. The model order reduction was performed by both PVL (Padé) and PRIMA using five poles. Although all of the poles from the Padé approximation were stable (i.e., negative real parts), the overall system was clearly unstable in Fig. 5. Note that the fifth-order approximation from PRIMA is indistinguishable from the exact response.

B. Coupled Noise For a 2-Bit Bus

In Fig. 6, a 2-bit bus driven by CMOS inverters is shown. One of the drivers is switching while the other is quiet. The interconnect, consisting of 40 coupled *RLC* sections, is modeled as a four-port and reduced by PRIMA. Transient

 TABLE I

 RUN TIME COMPARISONS OF DIRECT REALIZATION

 AND y-PARAMETER-BASED SIMULATIONS

Exact	Direct Realization	Y-parameter based
17.78 sec	0.6 with 8 poles	0.18 s. with 8 poles
	3.98 s. with 16 poles	0.28 s. with 16 poles
	10.29 s. with 24 poles	0.32 s. with 24 poles



Fig. 7. Six coupled transmision lines forming a 12 port.



Fig. 8. $Y_{11}(s)$ in frequency domain for six coupled TR lines.



Fig. 9. Three-port consisting of a large lumped RLC circuit.

analysis is done using recursive convolution. The time-domain waveforms at the load end are compared for various order of approximations. Since this is a four-port, an eight-pole approximation corresponds to matching only m_0 and m_1 generated by four different sources. The plot shows that, in the time domain, even the coupled noise can be accurately simulated using the eight poles from PRIMA. Although, in this example, the inductance of the interconnect is exaggerated to make things worse, it is seen that an approximation of order 8 is enough to capture the coupled noise from the active driver to the quiet load end.

To compare the difference between direct realization and yparameter-based simulation (i.e., recursive convolution here), the reduced-order circuit (via PRIMA) is simulated using both techniques. The run times are given in Table I. Although the circuit is relatively small (i.e., G is only 300 × 300), the gain in using PRIMA reduced macromodel and y-parameterbased simulation is about 50×. For larger circuits, this gain is expected to be much larger.

C. Six Coupled Transmission Lines

As a second example, we analyzed a 12 port containing six coupled transmission lines modeled by 40 coupled *RLC* sections (Fig. 7). The input admittance $(Y_{11}(s))$, reduced by block Arnoldi, MPVL, and PRIMA, are compared with the exact input admittance original in Fig. 8 using 48 poles. Block Arnoldi captures the exact response up to 16 GHz, while MPVL and PRIMA match up to 28 GHz. When the order of approximation is increased to 72 poles, it is observed that the frequency spectrum is captured up to 60 GHz by MPVL and PRIMA.

D. Large Coupled RLC Circuit

The third example is a three-port, composed of a densely coupled *RLC* circuit shown in Fig. 9. Approximations were performed using 25 poles for the three methods. As can be observed from Fig. 10, PRIMA and MPVL captures the entire frequency spectrum.



Fig. 10. $Y_{12}(s)$ for the three-port in Fig. 9.

VI. CONCLUSION

This paper presented PRIMA, a novel algorithm for producing provably passive macromodels for arbitrary RLC circuits. A simple implementation of PRIMA given here uses the block Arnoldi algorithm to generate the vectors needed for applying the transformations to the MNA matrices. Results show that the approach tends to be comparable to MPVL in terms of frequency-domain accuracy, but superior in that it guarantees the passivity that is critical for time-domain analyzes. Using the same principles that were introduced in PRIMA, it is possible to obtain passive reduced-order models for general RLC circuits in several ways. Further extensions to PRIMA have already been demonstrated. In [13], the moment vectors from different frequency expansion points were used in forming the block Krylov space to increase the accuracy. In [8], it is demonstrated that it is possible to obtain the block Krylov space used in PRIMA via a J-symmetric Lanczos process with improvements in run time.

REFERENCES

- L. T. Pillage and R. A. Rohrer, "Asymptotic waveform evaluation for timing analysis," *IEEE Trans. Computer-Aided Design*, vol. 9, pp. 352–366, Apr. 1990.
- [2] P. Feldmann and R. W. Freund, "Efficient linear circuit analysis by Padé approximation via the Lanczos process," *IEEE Trans. Computer-Aided Design*, vol. 14, pp. 639–649, May 1995.
- [3] K. J. Kerns, I. L. Wemple, and A. T. Yang, "Stable and efficient reduction of substrate model networks using congruence transforms," in *IEEE/ACM Proc. ICCAD*, Nov. 1995, pp. 207–214.
- [4] L. M. Silveira, M. Kamon, and J. White, "Efficient reduced-order modeling of frequency-dependent coupling inductances associated with 3-D interconnect structures," in *IEEE/ACM Proc. DAC*, June 1995, pp. 376–380.
- [5] P. Feldmann and R. W. Freund, "Reduced-order modeling of large linear subcircuits via a block Lanczos algorithm," in *IEEE/ACM Proc. DAC*, June 1995, pp. 474–479.
- [6] D. L. Boley, "Krylov space methods on state-space control models," *Circuits Syst. Signal Processing*, vol. 13, no. 6, pp. 733–758, 1994.
- [7] E. J. Grimme, "Krylov projection methods for model reduction," Ph.D. dissertation, Univ. Illinois, Urbana–Champaign, 1997.
- [8] R. Freund, "Reduced-order modeling techniques based on Krylov subspaces and their use in circuit simulation," Numer. Anal. Manuscript 98-3-02, Bell Lab., Murray Hill, NJ, Feb. 1998.

- [9] L. M. Silveira, M. Kamon, I. Elfadel, and J. White, "A coordinatetransformed Arnoldi algorithm for generating guaranteed stable reducedorder models of arbitrary RLC circuits," in *IEEE/ACM Proc. ICCAD*, Nov. 1996, pp. 288–294.
- [10] K. J. Kerns, "Accurate and stable reduction of RLC networks using split congruence transformations," Ph.D. dissertation, Univ. Washington, Sept. 1996.
- [11] B. D. Anderson and S. Vongpanitlerd, *Network Analysis and Synthesis*. Englewood Cliffs, NJ: Prentice-Hall, 1973.
- [12] R. A. Rohrer and H. Nosrati, "Passivity considerations in stability studies of numerical integration algorithms," *IEEE Trans. Circuits Syst.*, vol. CAS-28, pp. 857–866, Sept. 1981.
- [13] I. M. Elfadel and D. D. Ling, "A block rational Arnoldi algorithm for multipoint passive model-order reduction of multiport RLC networks," in *IEEE/ACM Proc. ICCAD*, Nov. 1997, pp. 66–71.
- [14] E. S. Kuh and R. A. Rohrer, *Theory of Linear Active Networks*. San Francisco: Holden-Day, 1967.
- [15] L. W. Nagel, "SPICE2, A computer program to simulate semiconductor circuits," Tech. Rep. ERL-M520, Univ. California, Berkeley, May 1975.
- [16] V. Raghavan, J. E. Bracken, and R. A. Rohrer, "AWESpice: A general tool for the accurate and efficient simulation of interconnect problems," in *IEEE/ACM Proc. DAC*, June 1992, pp. 87–92.
 [17] S. Y. Kim, N. Gopal, and L. T. Pillage, "Time-domain macromodels"
- [17] S. Y. Kim, N. Gopal, and L. T. Pillage, "Time-domain macromodels for VLSI interconnect analysis," *IEEE Trans. Computer-Aided Design*, vol. 13, pp. 1257–1270, Oct. 1994.
- [18] T. L. Quarles, "The SPICE3 implementation guide," Tech. Rep. Memo ERL-M89/44, Univ. California, Berkeley, 1989.



Altan Odabasioglu received the B.S. degree in electrical engineering from Bilkent University, Ankara, Turkey, in 1995, and the M.S. degree in electrical and computer engineering from Carnegie Mellon University, Pittsburgh, PA, in 1996.

He was a summer intern at Harris Semiconductors, Melbourne, FL, in 1996 and at Strategic CAD Labs at Intel Corporation, Hillsboro, OR, in 1997. He is currently with Monterey Design Systems, Sunnyvale, CA, and working toward the Ph.D. degree at Carnegie Mellon University. His research

interests include numerical aspects of circuit simulation and interconnect analysis.



Mustafa Celik (S'89–M'90) received the B.S. degree from Middle East Technical University, Ankara, Turkey, in 1988, and the M.S. and Ph.D. degrees from Bilkent University, Ankara, Turkey, in 1991 and 1994, respectively, all in electrical engineering.

After graduation, he held Research Associate positions, first in the Department of Electrical and Computer Engineering, University of Arizona, Tucson, and then in the Department of Electrical and Computer Engineering, Carnegie Mellon University,

Pittsburgh, PA. In the summer of 1997, he was with Motorola, Inc. At the end of 1997, he joined the Pacific Numerix Corporation, Scottsdale, AZ, as a Staff Development Scientist. His research interests include VLSI interconnect analysis and circuit simulation.

Lawrence T. Pileggi (M'85–SM'94), for a photograph and biography, see p. 49 of the January 1998 issue of this TRANSACTIONS.