Real Operational Amplifiers

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Overview

Real Operational Amplifiers

Packages

Figure: Single op-amp LM741 IC in SMD and through-hole packages.

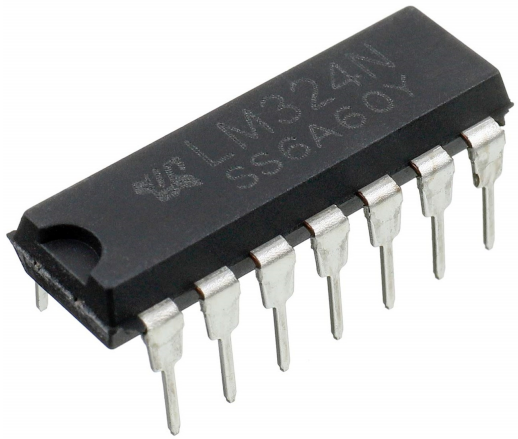


Figure: Quadruple op-amp LM324 IC in through-hole packages.

Internal Circuit

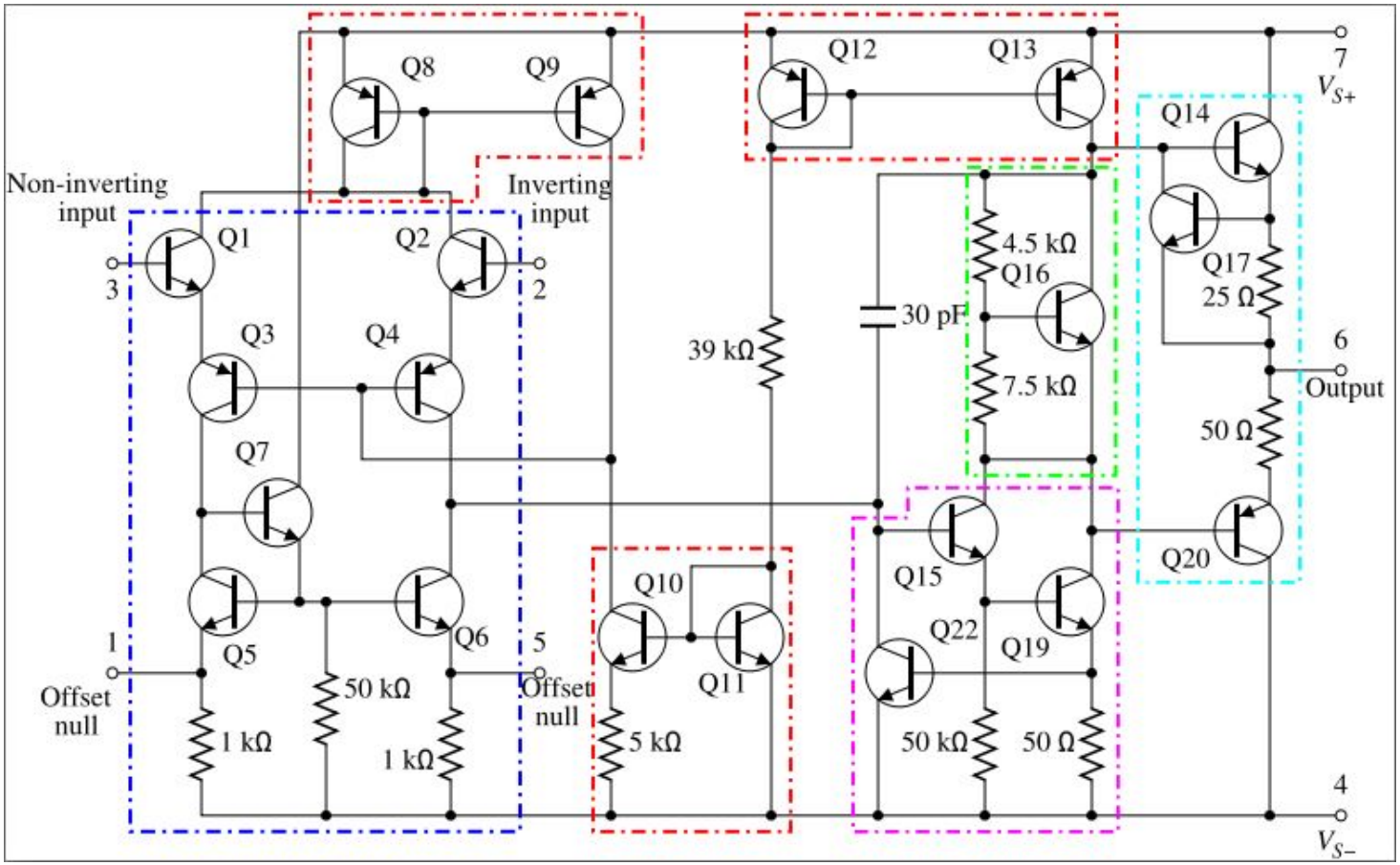
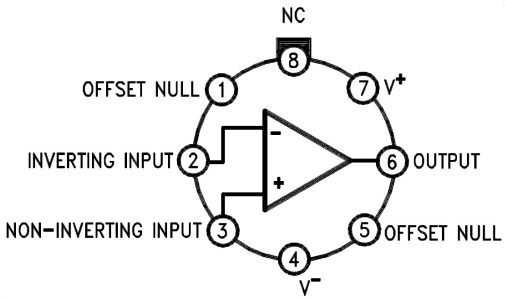


Figure: LM741 internal circuit.

Catalog Information

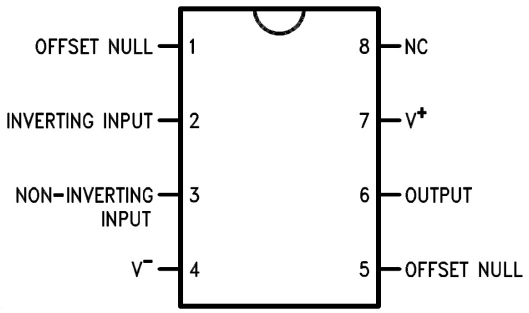
Pin diagram

LMC Package
8-Pin TO-99
Top View



LM741H is available per JM38510/10101

NAB Package
8-Pin CDIP or PDIP
Top View



Pin Functions

PIN		I/O	DESCRIPTION
NAME	NO.		
INVERTING INPUT	2	I	Inverting signal input
NC	8	N/A	No Connect, should be left floating
NONINVERTING INPUT	3	I	Noninverting signal input
OFFSET NULL	1, 5	I	Offset null pin used to eliminate the offset voltage and balance the input voltages.
OFFSET NULL			
OUTPUT	6	O	Amplified signal output
V+	7	I	Positive supply voltage
V–	4	I	Negative supply voltage

Figure: LM741 pin diagram.

Absolute Maximum Ratings

		MIN	MAX	UNIT
Supply voltage	LM741, LM741A		±22	V
	LM741C		±18	
Power dissipation (4)			500	mW
Differential input voltage			±30	V
Input voltage (5)			±15	V
Output short circuit duration		Continuous		
Operating temperature	LM741, LM741A	–50	125	°C
	LM741C	0	70	
Junction temperature	LM741, LM741A		150	°C
	LM741C		100	
Soldering information	PDIP package (10 seconds)		260	°C
	CDIP or TO-99 package (10 seconds)		300	°C
Storage temperature, T_{stg}		–65	150	°C

Figure: LM741 absolute maximum ratings.

Electrical Characteristics

PARAMETER		TEST CONDITIONS		MIN	TYP	MAX	UNIT
Input offset voltage		$R_S \le 10\ k\Omega$	$T_A = 25°C$		1	5	mV
			$T_{AMIN} \le T_A \le T_{AMAX}$			6	mV
Input offset voltage adjustment range		$T_A = 25°C$, $V_S = \pm 20\ V$			±15		mV
Input offset current		$T_A = 25°C$			20	200	nA
		$T_{AMIN} \le T_A \le T_{AMAX}$			85	500	
Input bias current		$T_A = 25°C$			80	500	nA
		$T_{AMIN} \le T_A \le T_{AMAX}$				1.5	µA
Input resistance		$T_A = 25°C$, $V_S = \pm 20\ V$		0.3	2		MΩ
Input voltage range		$T_{AMIN} \le T_A \le T_{AMAX}$		±12	±13		V
Large signal voltage gain		$V_S = \pm 15\ V$, $V_O = \pm 10\ V$, $R_L \ge 2\ k\Omega$	$T_A = 25°C$	50	200		V/mV
			$T_{AMIN} \le T_A \le T_{AMAX}$	25			
Output voltage swing		$V_S = \pm 15\ V$	$R_L \ge 10\ k\Omega$	±12	±14		V
			$R_L \ge 2\ k\Omega$	±10	±13		
Output short circuit current		$T_A = 25°C$			25		mA
Common-mode rejection ratio		$R_S \le 10\ \Omega$, $V_{CM} = \pm 12\ V$, $T_{AMIN} \le T_A \le T_{AMAX}$		80	95		dB
Supply voltage rejection ratio		$V_S = \pm 20\ V$ to $V_S = \pm 5\ V$, $R_S \le 10\ \Omega$, $T_{AMIN} \le T_A \le T_{AMAX}$		86	96		dB
Transient response	Rise time	$T_A = 25°C$, unity gain			0.3		µs
	Overshoot				5%		
Slew rate		$T_A = 25°C$, unity gain			0.5		V/µs
Supply current		$T_A = 25°C$			1.7	2.8	mA
Power consumption		$V_S = \pm 15\ V$	$T_A = 25°C$		50	85	mW
			$T_A = T_{AMIN}$		60	100	
			$T_A = T_{AMAX}$		45	75	

Figure: LM741 electrical characteristics.

Layout Guidelines

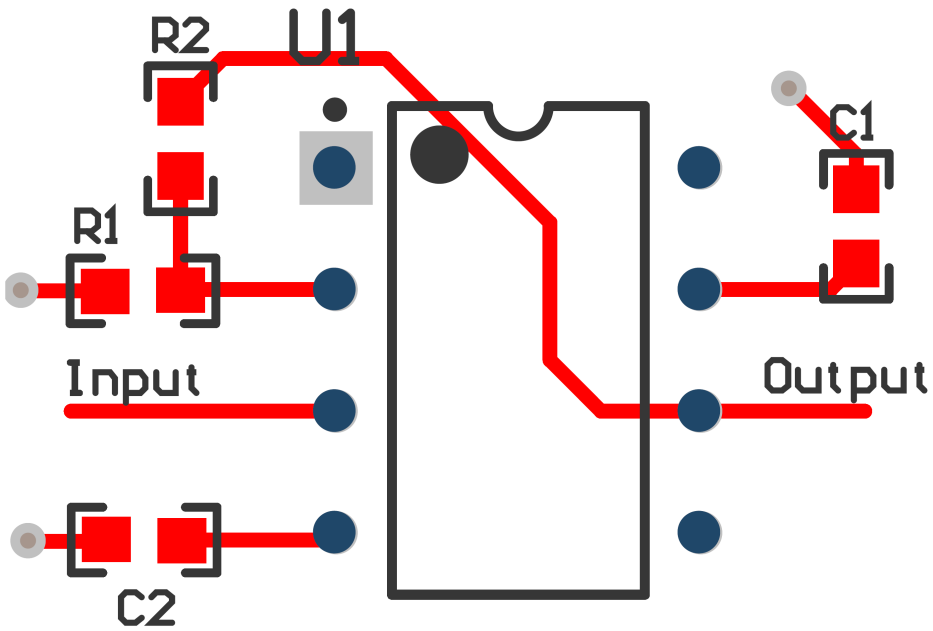


Figure: LM741 layout guidelines used for PCB design.

Package Dimensions

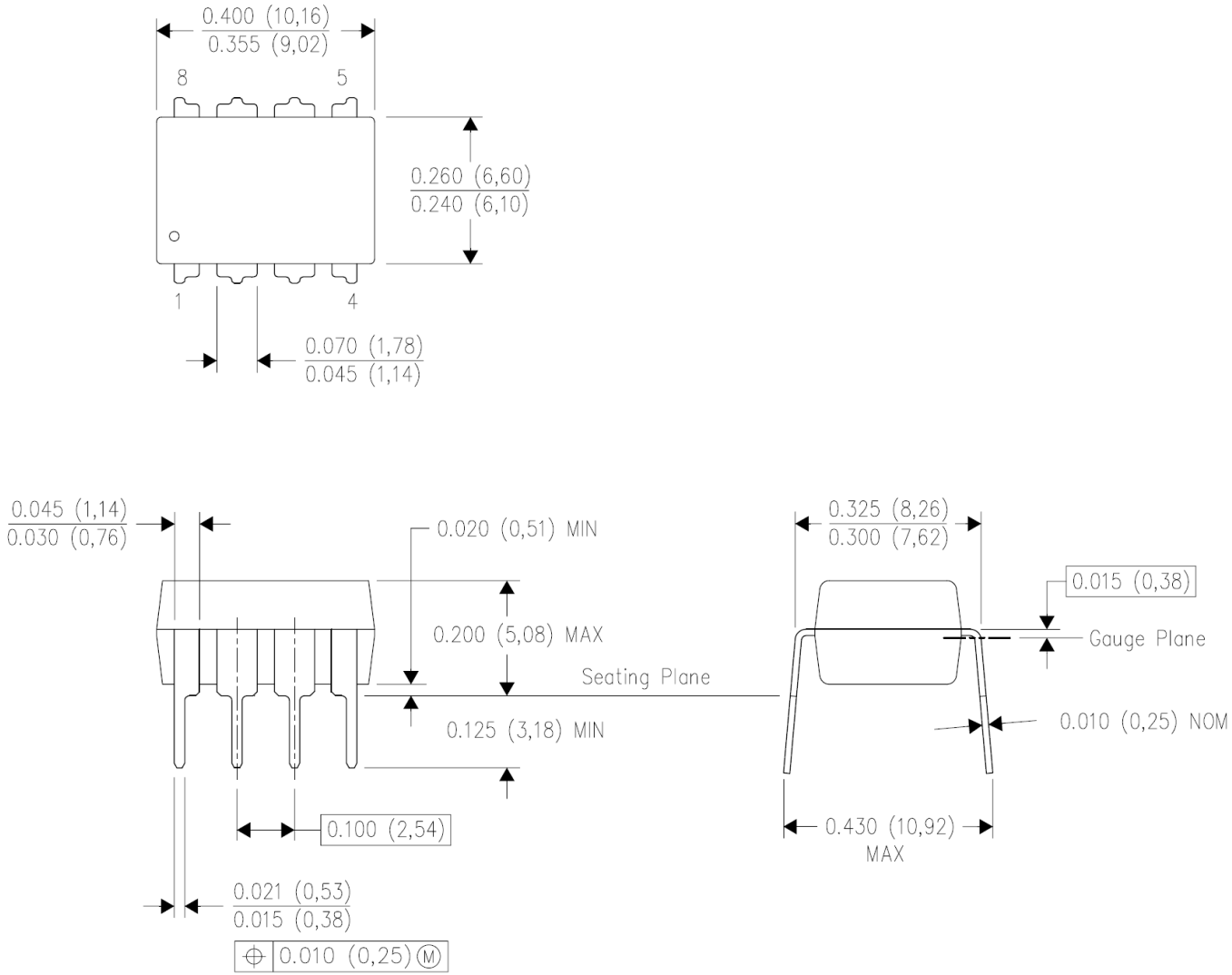
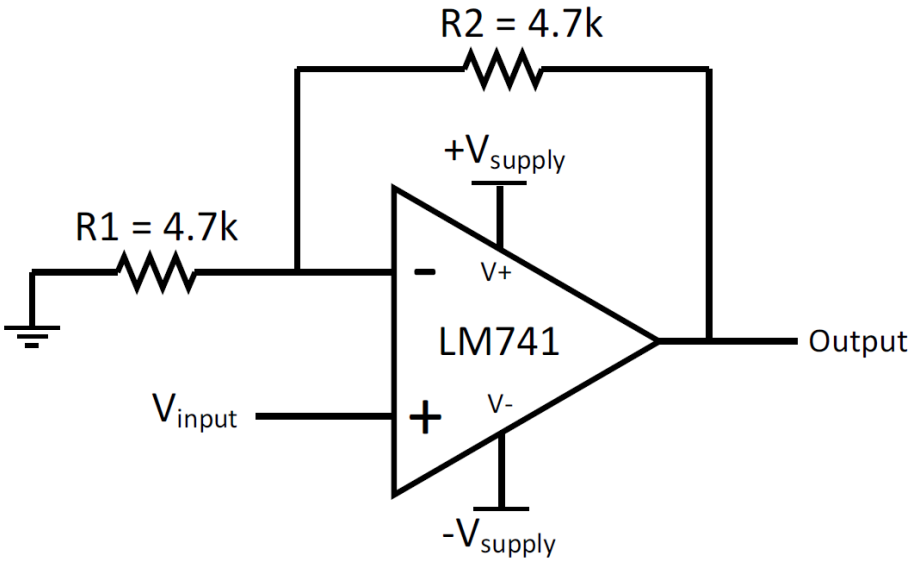


Figure: LM741 package dimensions used for element footprint.

Applications



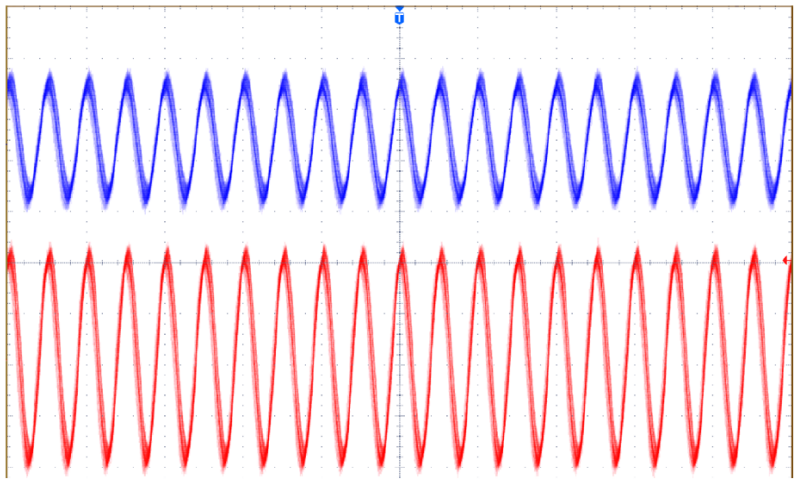


Figure: LM741 Noninverting Amplifier Circuit.

Saturation

Offset

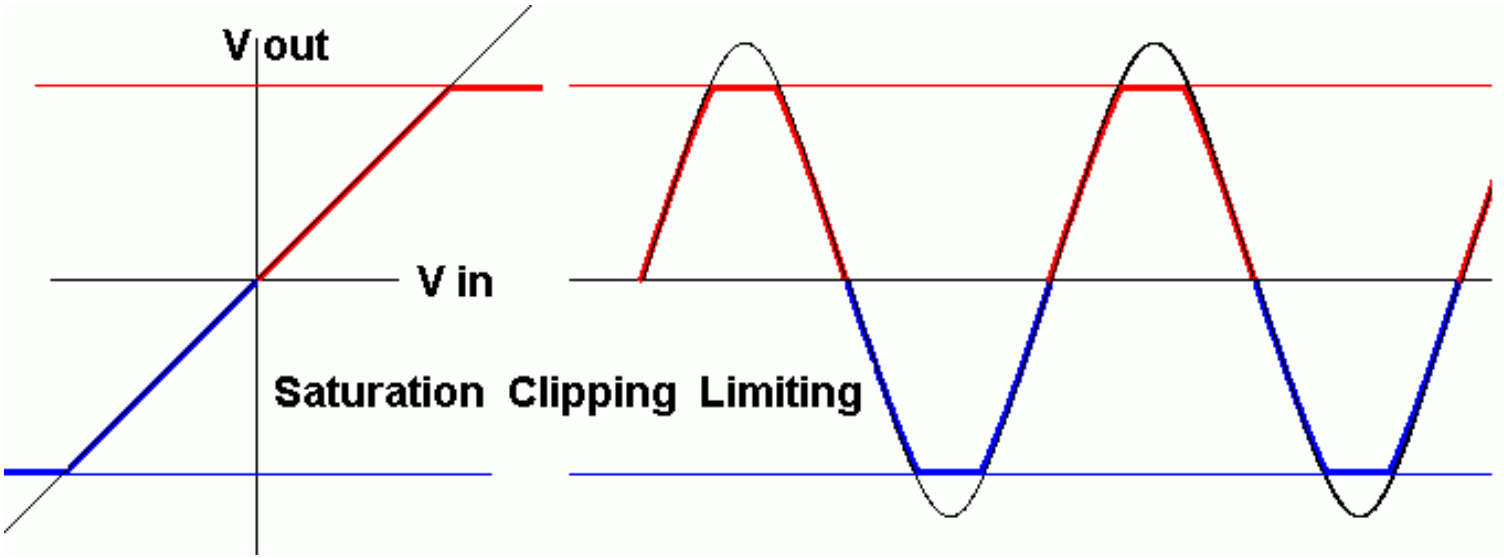
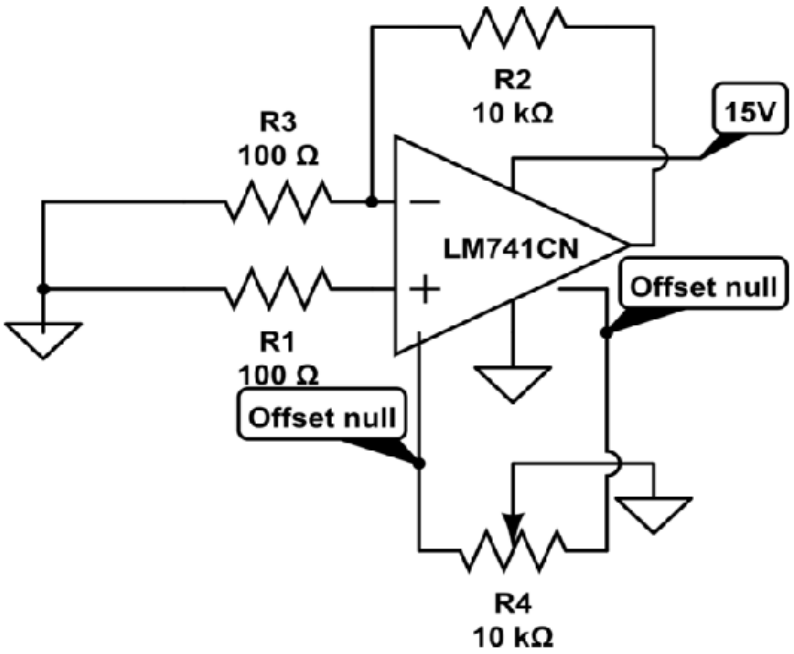


Figure: Output voltage saturation.

Offset

Offset



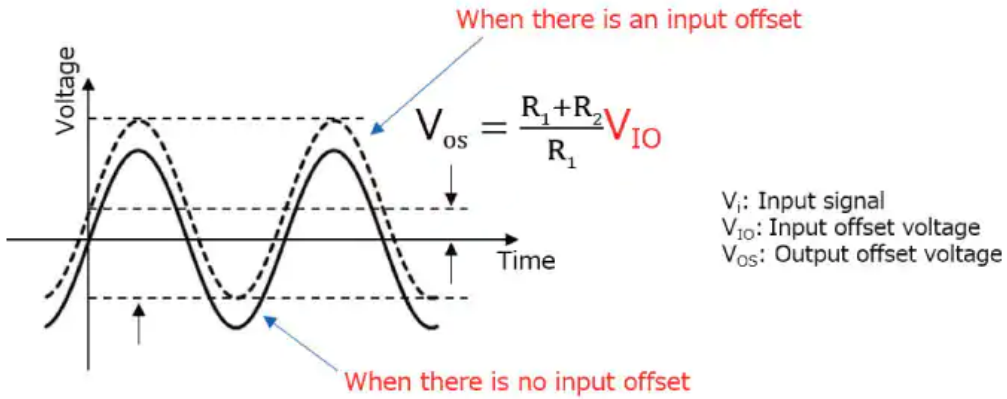


Figure: Offset nulling pins.

Slew Rate

Slew Rate

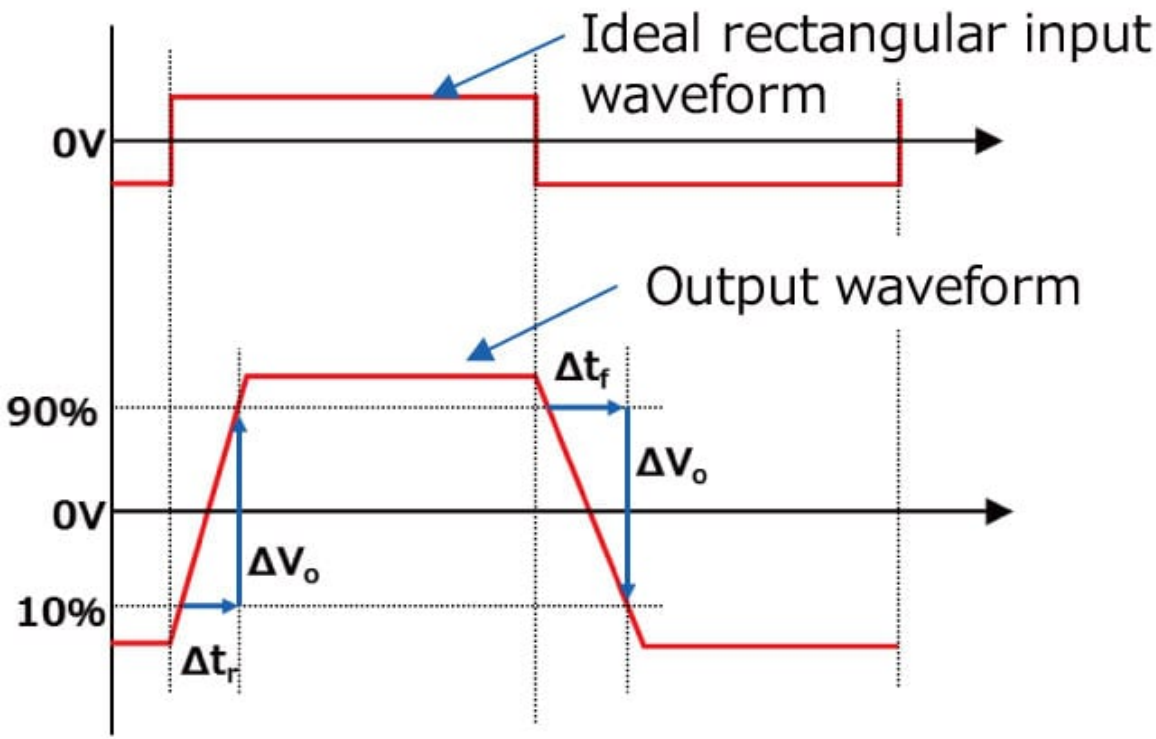


Figure: Slew rate concept.

Slew Rate

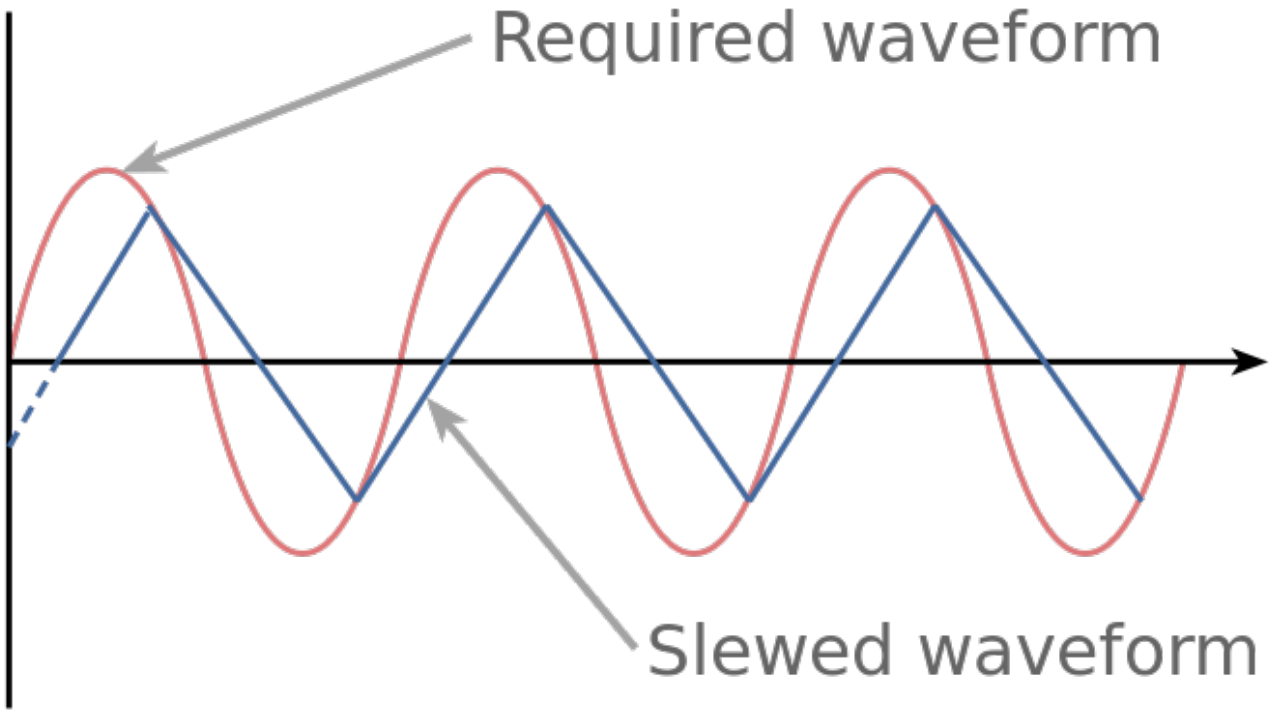


Figure: Slew rate impact on the output waveform.

Gain-Bandwidth Product

Gain-Bandwidth Product

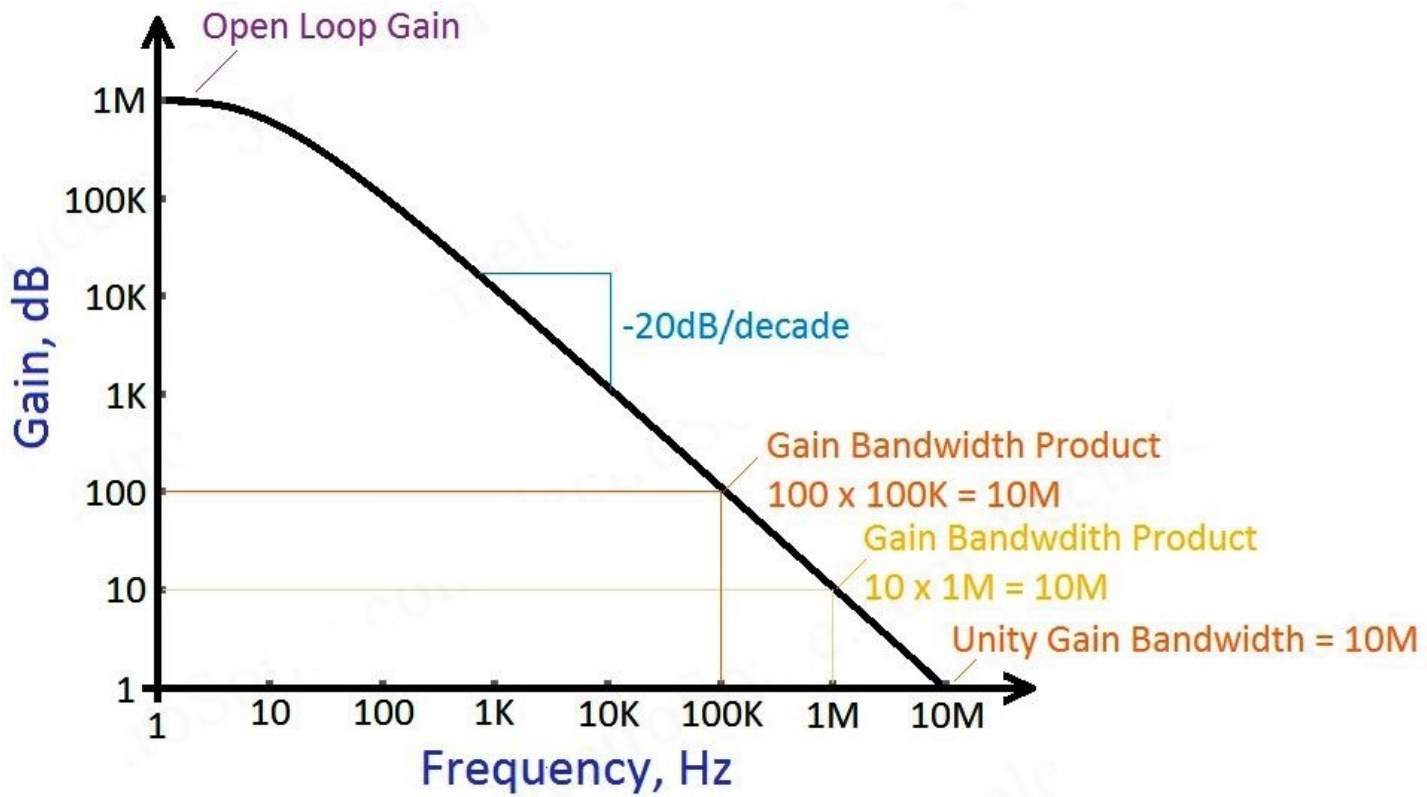


Figure: Gain-bandwidth product.

The End