MATHEMATICAL QUESTIONS

Question 1

Find the equivalent resistance of the ladder network in Fig. 1.



Let *R* be the equivalent resistor seen from the terminal point A and B. Since the ladder has infinite length, the same equivalent resistor *R* is seen from each vertical resistor R_2 . Therefore, $R = R_1 + R_2 ||R = R_1 + R_2 R/(R_2 + R)$. This is a second-order equation, whose acceptable solution is

$$R = \frac{R_1 + \sqrt{R_1^2 + 4R_1R_2}}{2}$$

Question 2

How are Δ and T resistor networks in Fig. 2 equivalent?



Figure 2: Two well-known equivalent resistor circuits. (a) Δ network. (b) T network.



second solution: The networks should behave the same for any values of i_a, i_b, i_c, i_d and v_a, v_b, v_c, v_d . Especially, when $i_b = 0$, then $v_c - v_a = i_a R_1 + i_a R_3 = i_a R_A ||(R_B + R_C)$, which results in $R_1 + R_3 = R_A ||(R_B + R_C)$. Similarly, if $i_a = 0, R_2 + R_3 = R_C ||(R_B + R_A)$, and if $i_c + i_d = 0, R_1 + R_2 = R_B ||(R_C + R_A)$. These equations lead to $R_1 = \frac{R_A R_B}{R_A + R_B + R_C}$, $R_2 = \frac{R_B R_C}{R_A + R_B + R_C}$

, and

$$R_3 = \frac{R_A R_C}{R_A + R_B + R_C}$$

. Further,

$$R_A = \frac{R_1 R_2 + R_2 R_3 + R_3 R_1}{R_2}$$

$$R_B = \frac{R_1 R_2 + R_2 R_3 + R_3 R}{R_3}$$

, and

$$R_C = \frac{R_1 R_2 + R_2 R_3 + R_3 R}{R_1}$$

Question 3

Consider the characteristic curves of the two NTI inductors shown in Fig. 3.



Figure 3: (a) Characteristic curves of two NTI inductors. (b) parallel connection. (c) series connection.

(a) Plot the characteristic curve of the parallel connection of the two inductors. Assume that the initial conditions are zero.



(b) Plot the characteristic curve of the series connection of the two inductors. Assume that the initial conditions are zero.

At first note that the current flowing into the series connection should be confined within [0, 2]. Any current outside this interval is not acceptable for one of the inductors. Each characteristic curve has two arms over [0, 2]. So, there are four cases for the summation of the fluxes at the same current as shown in the following curve.

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Question 4

Consider the diode circuit shown in Fig.4.



Figure 4: Full diode bridge rectifier.

(a) Plot the input-output characteristic curve, i.e., V_{out} versus V_{in} .

If V_{in} is positive, diodes D_1 and D_4 conduct while D_2 and D_3 are reverse biased, so

 $V_{out} = V_{in}$

If V_{in} is negative, diodes D_2 and D_3 conduct while D_1 and D_4 are reverse biased, so

 $V_{out} = -V_{in}$

Hence,

 $V_{out} = |V_{in}|$

The characteristic curve is shown below.



(b) Assume that $V_{in} = A\cos(\omega t + \theta)$. Plot V_{out} versus time t.

According to part (a), if $V_{in} = A\cos(\omega t + \theta)$ is positive, then

 $V_{out} = A\cos(\omega t + \theta)$

and if $V_{in} = A\cos(\omega t + \theta)$ is negative, then

$$V_{out} = -V_{in} = -A\cos(\omega t + \theta)$$

Hence,

 $V_{out} = |A\cos(\omega t + \theta)|$

A sample plot of V_{out} is shown below.



If a resistor load is connected to V_{out} , the circuit keeps the load current flowing solely in one direction. So, it acts like a rectifier.

Question 5

Two LTI capacitors and two LTI inductors are respectively going to be connected in parallel and series at t = 0, as shown in Fig. 5.

Figure 5: (a) Parallel connection of two LTI capacitors. (b) series connection of two LTI inductors.

(a) Calculate $\epsilon(0^-)$ and $\epsilon(0^+)$, the absolute electrical energy before and after connection, for the capacitors and show that $\epsilon(0^-) \le \epsilon(0^+)$. Is the energy conserved? Provide enough explanation.

First write a kvl for 0^+ as

 $V_1(0^+) = V_2(0^+) = V(0^+)$

Because of the conservation of charge

$$Q(0^{-}) = Q(0^{+})$$

So, we have

$$C_1 V_1(0^-) + C_2 V_2(0^-) = (C_1 + C_2) V(0^+)$$
$$V(0^+) = \frac{C_1 V_1(0^-) + C_2 V_2(0^-)}{C_1 + C_2}$$

Thus,

$$\epsilon(0^{-}) = \frac{1}{2}C_1V_1^2(0^{-}) + \frac{1}{2}C_2V_2^2(0^{-})$$

$$\epsilon(0^{+}) = \frac{1}{2}(C_1 + C_2)V^2(0^{+}) = \frac{1}{2}\frac{(C_1V_1(0^{-}) + C_2V_2(0^{-}))^2}{C_1 + C_2}$$

Finally we have,

$$\begin{aligned} \epsilon(0^+) &\leq \epsilon(0^-) \\ \Leftrightarrow \frac{1}{2} \frac{(C_1 V_1(0^-) + C_2 V_2(0^-))^2}{C_1 + C_2} &\leq \frac{1}{2} C_1 V_1^2(0^-) + \frac{1}{2} C_2 V_2^2(0^-) \\ \Leftrightarrow C_1^2 V_1^2(0^-) + C_2^2 V_2^2(0^-) 2 + 2 C_1 C_2 V_1(0^-) V_1(0^+) &\leq C_1 (C_1 + C_2) V_1^2(0^-) + C_2 (C_1 + C_2) V_2^2(0^-) \\ &\Leftrightarrow 2 V_1(0^-) V_1(0^+) &\leq V_2^2(0^-) + V_1^2(0^-) \\ &\Leftrightarrow 0 &\leq (V_2(0^-) - V_1(0^-))^2 \end{aligned}$$

The missing energy actually goes into the kinetic energy of conducting charges getting transferred from C_1 to C_2 . In reality, the missing energy is dissipated in non-zero resistance of the wires.

(b) Calculate $\epsilon(0^-)$ and $\epsilon(0^+)$, the absolute magnetic energy before and after connection, for the inductors and show that $\epsilon(0^-) \leq \epsilon(0^+)$. Is the energy conserved? Provide enough explanation.

first write kcl for 0^+

$$i_1(0^+) = i_2(0^+) = i(0^+)$$

because of the conservation of flux

$$\phi(0^-) = \phi(0^+)$$

so we have,

$$L_1 i_1(0^-) + L_2 i_2(0^-) = (L_1 + L_2)i(0^+)$$
$$i(0^+) = \frac{L_1 i_1(0^-) + L_2 i_2(0^-)}{L_1 + L_2}$$

SO,

$$\epsilon(0^{-}) = \frac{1}{2}L_1i_1^2(0^{-}) + \frac{1}{2}L_2i_2^2(0^{-})$$

$$\epsilon(0^{+}) = \frac{1}{2}(L_1 + L_2)i^2(0^{+}) = \frac{1}{2}\frac{(L_1i_1(0^{-}) + L_2i_2(0^{-}))^2}{L_1 + L_2}$$

Finally, in a similar way as the previous part, one cane verify that

$$\epsilon(0^+) \le \epsilon(0^-)$$

The missing energy actually goes into the kinetic energy of conducting fluxes getting transferred from L_1 to L_2 . In reality, the missing energy is dissipated in non-zero resistance of the wires.

Question 6

Design a resistive one-port with linear resistors, ideal diodes, and independent sources that has the iv characteristic shown in Fig. 6.

Figure 6: A desired *iv* characteristic curve.

Now, we need to synthesize some sub-circuits with the iv characteristic similar to the different parts of the desired characteristic curve. Here, are some suggestions.

Figure 9: A parallel combination of a diode and a current source and its corresponding characteristic curve.

Question 7

Assume that the characteristic curve of a voltage-controlled NTI capacitor is described by q = f(v). Write a MATLAB/Python function that receives the characteristic curve along with an arbitrary input voltage waveform and generates the corresponding output current waveform. Plot the current waveform for a few sample NTI capacitors.

Here is a sample MATLAB implementation of the desired function. 1 function [i] = i_curve(f, v) 2 3 % take the derivative of f with respect to v 4 syms x; 5 fpv = matlabFunction(diff(f(x))); 6 7 % take the derivative of v with respect to t 8 syms t; 9 vpt = matlabFunction(diff(v(t))); 10 11 % create the current function 12 if (nargin(fpv)==0 && nargin(vpt)==0) i = @(t) fpv().*vpt(); 13 14 elseif (nargin(fpv)~=0 && nargin(vpt)==0) 15 i = @(t) fpv(v(t)).*vpt(); 16 elseif (nargin(fpv)==0 && nargin(vpt)~=0) i = @(t) fpv().*vpt(t); 17 18 else i = @(t) fpv(v(t)).*vpt(t);19 20 end 21 22 end

You may use the following mfile to call the developed function and see its results.

```
1 % clear all
2 % clc
3
4 % sample current curve
5 f = @(v) v^3;
6 v = @(t) t;
7 i = i_curve(f, v);
8
9 figure
10 tval = 0:0.001:1;
11 plot(tval, i(tval))
12 xlabel('t')
13 ylabel('i(t)')
14 box on
15 grid on
```

16 17 18 19	% sample current curve f = @(v) v^2; v = @(t) sin(t);
20	i = i_curve(f, v);
21	
22	figure
23	tval=0:0.001:5;
24	<pre>plot(tval, i(tval))</pre>
25	xlabel('t')
26	ylabel('i(t)')
27	box on
28	grid on

Two sample current curves are shown in Figs. 15 and 16.

BONUS QUESTIONS

Question 8

Consider the infinite grid of resistors shown in Fig. 17. All the resistors have a same resistance of R.

(a) Find the resistance between any two horizontal adjacent nodes $R_{0,1}$.

At first, pay your attention to the generalized version of the source transformation shown in Fig. 18. Since these transformations do not alter any KVL and KCL, they are valid. Now, consider Fig. 19, where a current source connects to nodes A and B. Applying the source transformation, we can break the current source into two currents sources. Note that one side of each current source connects to a location at the end of infinite network. Using superposition, we can consider the effect of each current source separately, as shown in Fig. 20. Due to symmetry, we have

$$v_1 = R\frac{i}{4}, v_2 = R\frac{i}{4} \Rightarrow v = v_1 + v_2 = R\frac{i}{2}$$

So,

$$R_{in} = R_{0,1} = \frac{v}{i} = \frac{R}{2}$$

(b) Find the resistance between any two vertical adjacent nodes $R_{1,0}$.

This part is exactly the same as the previous part due to symmetry of the grid. So, $R_{1,0} = \frac{R}{2}$.

(c) Find the resistance $R_{m,n}$ between two arbitrary nodes, which are m horizontal and n vertical steps away from each other. Note that this part may need complex mathematical calculations.

This part needs complex math. Visit this page for a detailed description of the solution.

Question 9

Return your answers by filling the LATEXtemplate of the assignment.

EXTRA QUESTIONS

Question 10

Feel free to solve the following questions from the book *"Basic Circuit Theory"* by C. Desoer and E. Kuh.

- 1. Chapter 3, question 3.
- 2. Chapter 3, question 6.
- 3. Chapter 3, question 8.
- 4. Chapter 3, question 11.
- 5. Chapter 3, question 12.
- 6. Chapter 3, question 15.
- 7. Chapter 3, question 19.
- 8. Chapter 3, question 20.