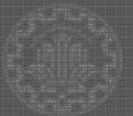


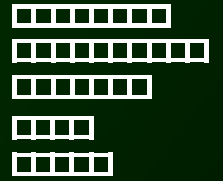
Session 6: Solid State Devices

Transferred Electron Effects



Outline

1. I
- 2.
- 3.
- 4.
- 5.



- ⊙ A
 - B
 - C
 - D
 - E
- ⊙ F
 - G
- ⊙ H
- ⊙ I
- ⊙ J



Outline

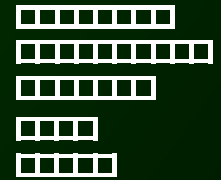
1.1	□□□□□□□□
2.	□□□□□□□□□□
3.	□□□□□□□
4.	□□□□
5.	□□□□

- Ref: Brennan and Brown
Sze and Ng, Chapter10



Transferred Electron Effects

1. I
- 2.
- 3.
- 4.
- 5.



k-space transfer

intrinsic property of the semiconductor, and as such cannot be readily engineered

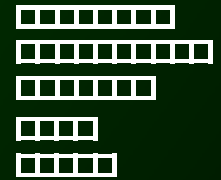
real-space transfer

induced artificially within a semiconductor system and as such can be engineered



K-Space Transfer

1. |
- 2.
- 3.
- 4.
- 5.



1-D continuity equation $\frac{\partial n}{\partial t} - \frac{1}{q} \frac{\partial J}{\partial x} = 0$

$$\frac{d\mathcal{E}}{dx} = -\frac{q\delta n}{\epsilon} \quad \delta n = n - n_0$$

$$J = q\mu_n n \mathcal{E} + qD_n \nabla_x n \quad \frac{1}{q} \frac{dJ}{dx} = D_n \frac{\partial^2 n}{\partial x^2} + \frac{1}{q\rho} \frac{d\mathcal{E}}{dx} \mathcal{E} \quad \rho = 1/q\mu_n n$$

$$\frac{1}{q} \frac{dJ}{dx} = D_n \frac{\partial^2 n}{\partial x^2} - \frac{n - n_0}{\rho\epsilon}$$

$$-\frac{dn}{dt} + D_n \frac{d^2 n}{dx^2} - \frac{n - n_0}{\rho\epsilon} = 0$$

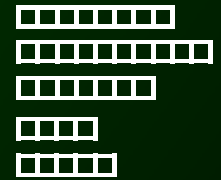
$$-\frac{d(n - n_0)}{dt} + D_n \frac{d^2(n - n_0)}{dx^2} - \frac{n - n_0}{\rho\epsilon} = 0$$

$$n - n_0 = u(x)T(t) \quad \frac{D_n}{u} \frac{d^2 u}{dx^2} - \frac{1}{\rho\epsilon} = \frac{1}{T} \frac{dT}{dt}$$



K-Space Transfer

1. |
- 2.
- 3.
- 4.
- 5.



$$\frac{D_n d^2 u}{u dx^2} - \frac{1}{\rho \epsilon} = \frac{1}{T} \frac{dT}{dt} \quad \text{Steady state} \quad \frac{d}{dx} = 0 \quad D_n \frac{d^2(n - n_0)}{dx^2} = \frac{n - n_0}{\rho \epsilon}$$

$$n - n_0 = A_1 e^{x/L_D} + A_2 e^{-x/L_D}$$

$$\text{Debye length } L_D = \sqrt{kT\epsilon/q^2 n_0}$$

$$n - n_0 = \delta n(0) e^{-x/L_D}$$

$$\frac{1}{T} \frac{dT}{dt} = -\frac{1}{\rho \epsilon}$$

$$T = A e^{-t/\rho \epsilon}$$

$$\tau = \rho \epsilon = \epsilon / q \mu_n n_0$$

$$n = n_0 + (n - n_0)_{t=0} e^{-t/\tau}$$

$$\tau >? < 0$$

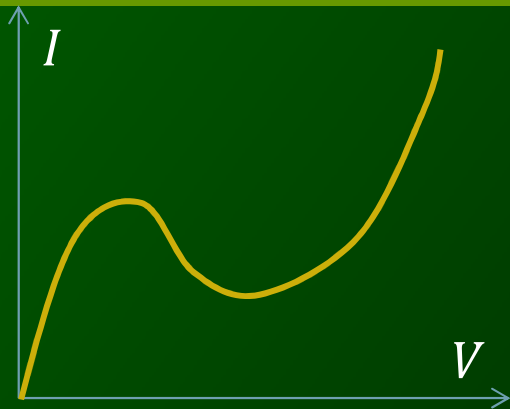
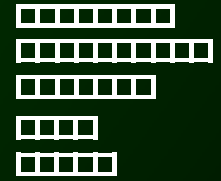
negative differential resistance (NDR)

$$\tau = \rho \epsilon = RC$$



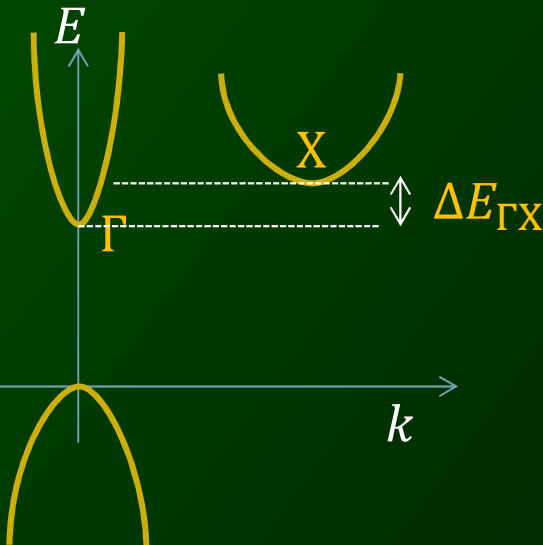
NDR

1. I
- 2.
- 3.
- 4.
- 5.



$$\sigma = q(\mu_{\Gamma}n_{\Gamma} + \mu_{\text{X}}n_{\text{X}})$$

$$\frac{d\sigma}{d\varepsilon} = q \left(\mu_{\Gamma} \frac{dn_{\Gamma}}{d\varepsilon} + \mu_{\text{X}} \frac{dn_{\text{X}}}{d\varepsilon} \right) + q \left(n_{\Gamma} \frac{d\mu_{\Gamma}}{d\varepsilon} + n_{\text{X}} \frac{d\mu_{\text{X}}}{d\varepsilon} \right)$$



$$n = n_{\Gamma} + n_{\text{X}} = \text{cte}$$

$$\frac{dn_{\Gamma}}{d\varepsilon} = - \frac{dn_{\text{X}}}{d\varepsilon}$$

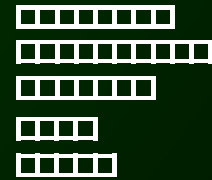
$$\mu_{\Gamma} \approx \varepsilon^p \quad \mu_{\text{X}} \approx \varepsilon^p$$

$$\frac{d\sigma}{d\varepsilon} = q(\mu_{\Gamma} - \mu_{\text{X}}) \frac{dn_{\Gamma}}{d\varepsilon} + q(\mu_{\Gamma}n_{\Gamma} + \mu_{\text{X}}n_{\text{X}}) \frac{p}{\varepsilon}$$



NDR

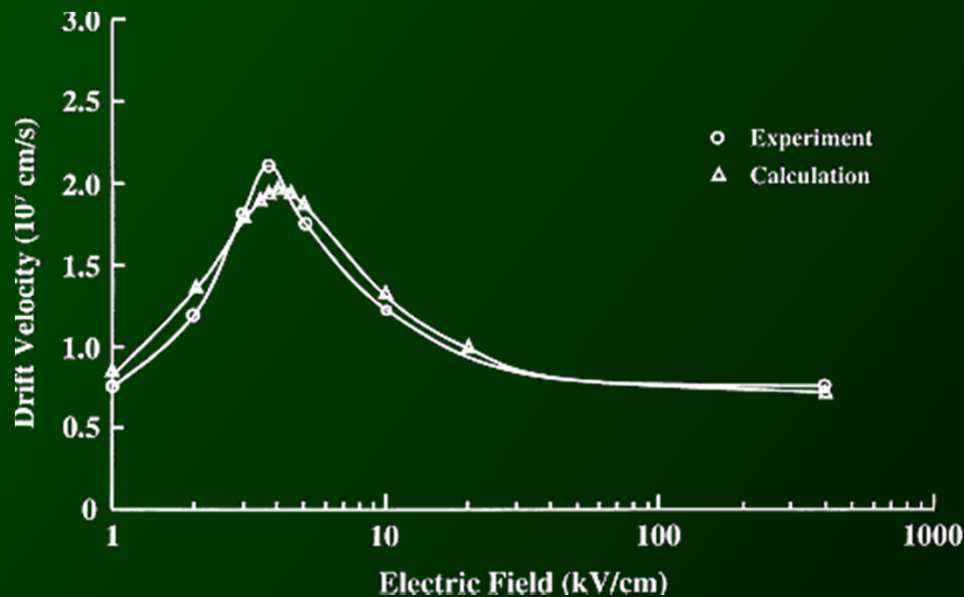
1. |
- 2.
- 3.
- 4.
- 5.



$$J = \sigma \varepsilon \quad \frac{d\sigma}{d\varepsilon} = q(\mu_{\Gamma} - \mu_X) \frac{dn_{\Gamma}}{d\varepsilon} + q(\mu_{\Gamma}n_{\Gamma} + \mu_Xn_X) \frac{p}{\varepsilon}$$

$$\frac{dJ}{d\varepsilon} = \sigma + \varepsilon \frac{d\sigma}{d\varepsilon} \quad \frac{dJ}{d\varepsilon} < 0 \quad -\frac{d\sigma/d\varepsilon}{\sigma/\varepsilon} > 1$$

$$\left[\frac{\mu_{\Gamma} - \mu_X}{\mu_{\Gamma} + (n_X/n_{\Gamma})\mu_X} \left(-\frac{\varepsilon}{n_{\Gamma}} \frac{dn_{\Gamma}}{d\varepsilon} \right) - p \right] > 1$$



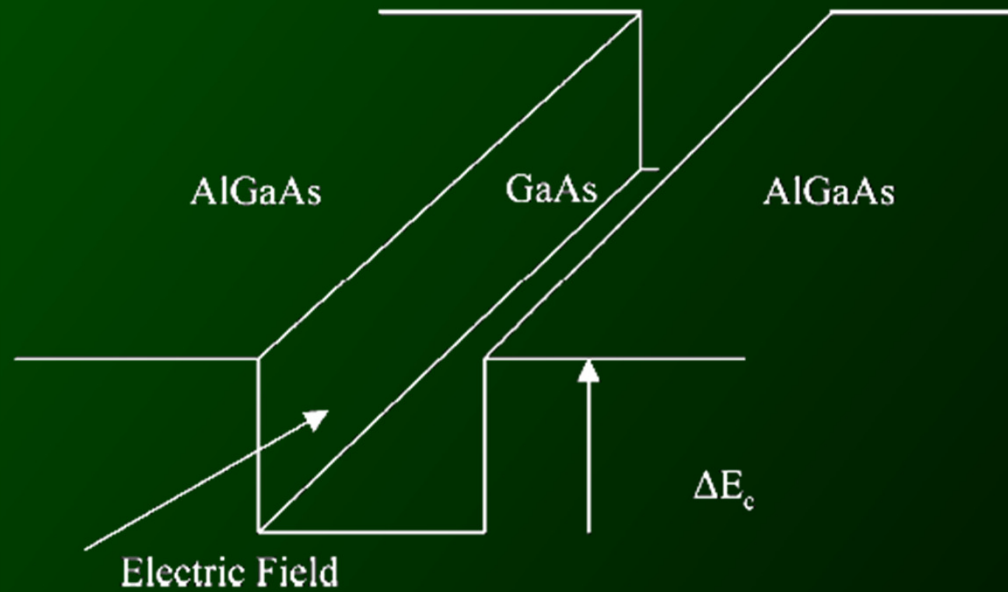
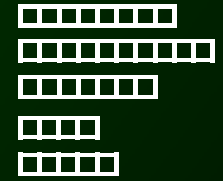
$$\frac{n_{\Gamma}}{n_X} = \frac{N_{\Gamma}}{N_X} e^{-\Delta E_{\Gamma X}/kT}$$

$$= \left(\frac{m_{\Gamma}}{m_X} \right)^{3/2} e^{-\Delta E_{\Gamma X}/kT}$$



Real-Space Transfer

1. |
- 2.
- 3.
- 4.
- 5.



GaAs

$$\mu \propto 1/m^*$$

$Al_xGa_{1-x}As$

$x < 45\%$

direct : Γ valley

$$m_{AlGaAs}^* > m_{GaAs}^*$$

$x > 45\%$

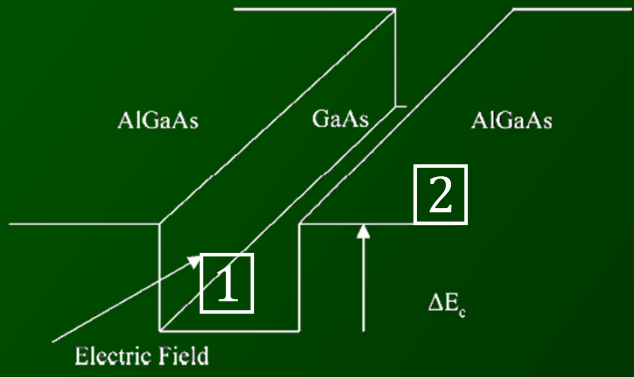
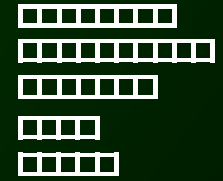
indirect : X valley

✓ NDR can occur



Real-Space Transfer

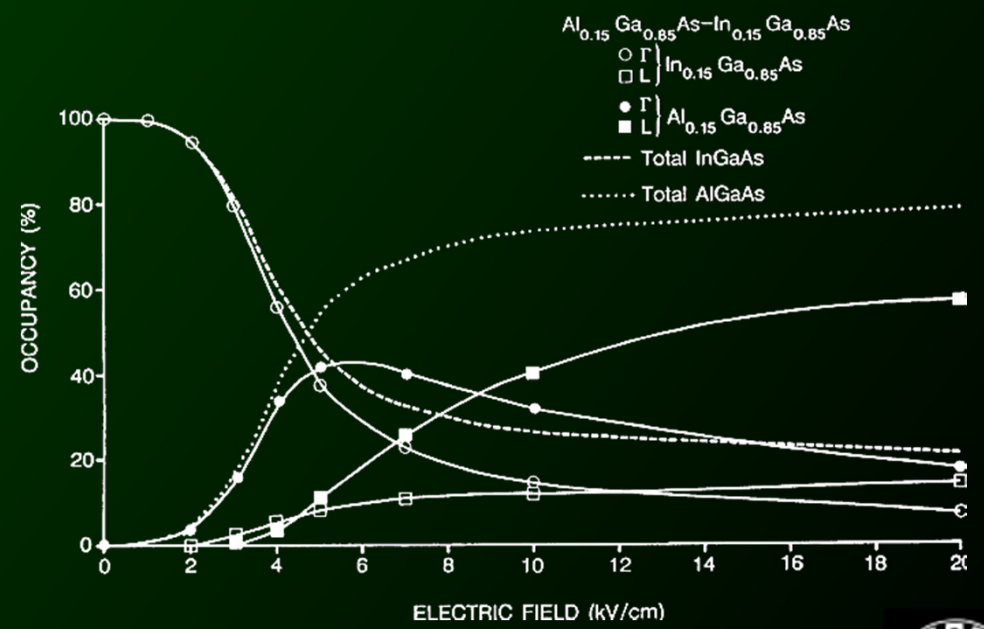
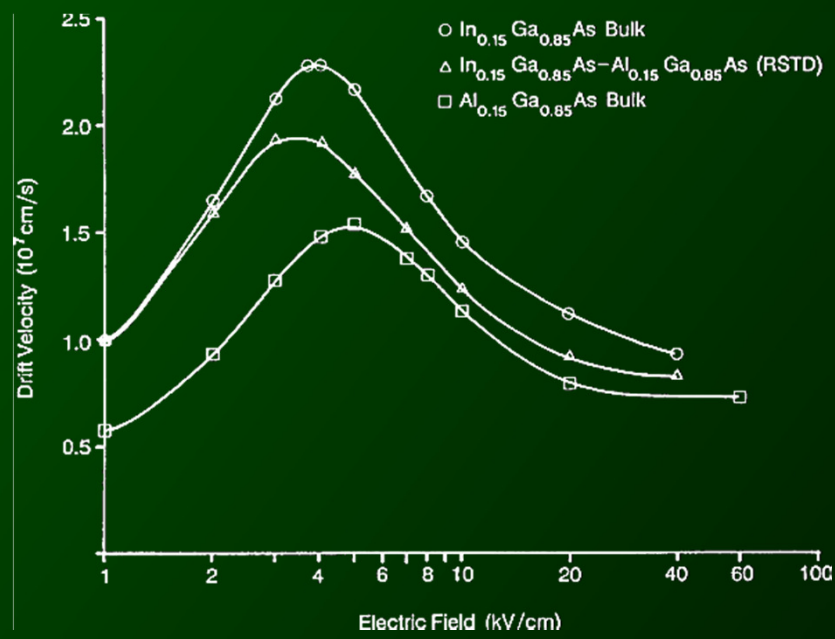
- 1.
- 2.
- 3.
- 4.
- 5.



$$\bar{v} = \frac{v_1 n_1 + v_2 n_2}{n_1 + n_2}$$

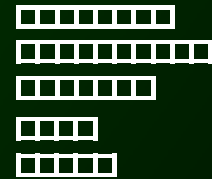
$$n = \int D(E) f(E) dE$$

$$\frac{n_2}{n_1} = \left(\frac{m_2}{m_1} \right)^{3/2} e^{-\Delta E_c / kT_e}$$



Consequences of NDR

1. |
- 2.
- 3.
- 4.
- 5.



Gunn (1963) observed experimentally microwave oscillations in bulk GaAs under the application of a bias

assume that diffusion can be neglected

$$J = qn\mu\mathcal{E}$$

$$\frac{\partial n}{\partial t} - \frac{1}{q} \frac{\partial J}{\partial x} = 0$$

$$\begin{aligned} \frac{dn}{dt} &= \frac{d}{dx}(n\mu\mathcal{E}) = \mathcal{E} \frac{d}{dx}(n\mu) + n\mu \frac{d\mathcal{E}}{dx} \\ &= \mathcal{E} \frac{d}{dx}(n\mu) - qn\mu \frac{n - n_0}{\epsilon} \\ &= \mathcal{E} \frac{d}{dx}(n\mu) - \frac{n - n_0}{\tau} \end{aligned}$$

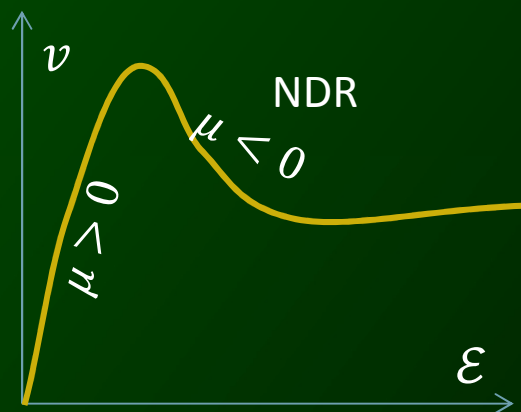
$$n - n_0 = u(x)T(t)$$

$$n = (n - n_0)e^{-t/\tau} + n_0$$

negative τ

$$n = (n - n_0)e^{t/\tau} + n_0$$

$$\mu = v/\mathcal{E}$$








$$t_{tr} = \frac{L}{v_D} > \tau = \frac{\epsilon}{qn\mu}$$

$$\rightarrow LN_D > \frac{\epsilon v_D}{q\mu} \sim 10^{12} \text{ cm}^{-2} \text{ for GaAs}$$



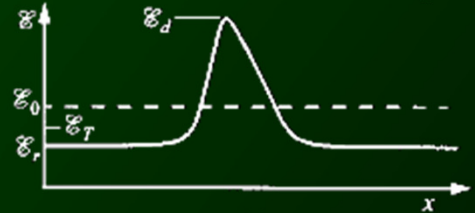
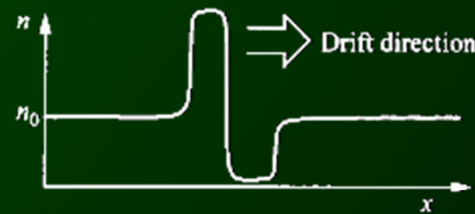
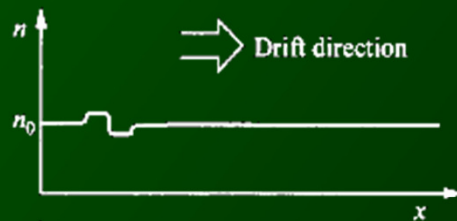
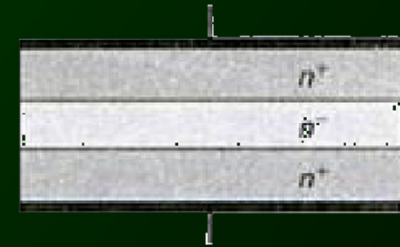
Gunn Diode

Transferred Electron-Effect Oscillators

1. I 
2. 
3. 
4. 
5. 

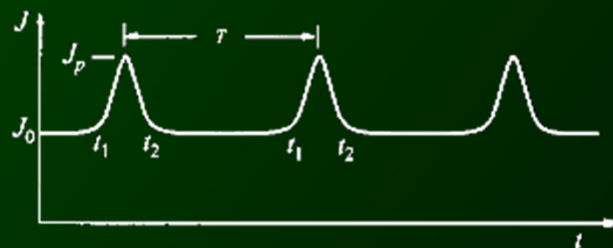


(a)



(b)

(c)



Functional Devices

- 1. I
- 2.
- 3.
- 4.
- 5.

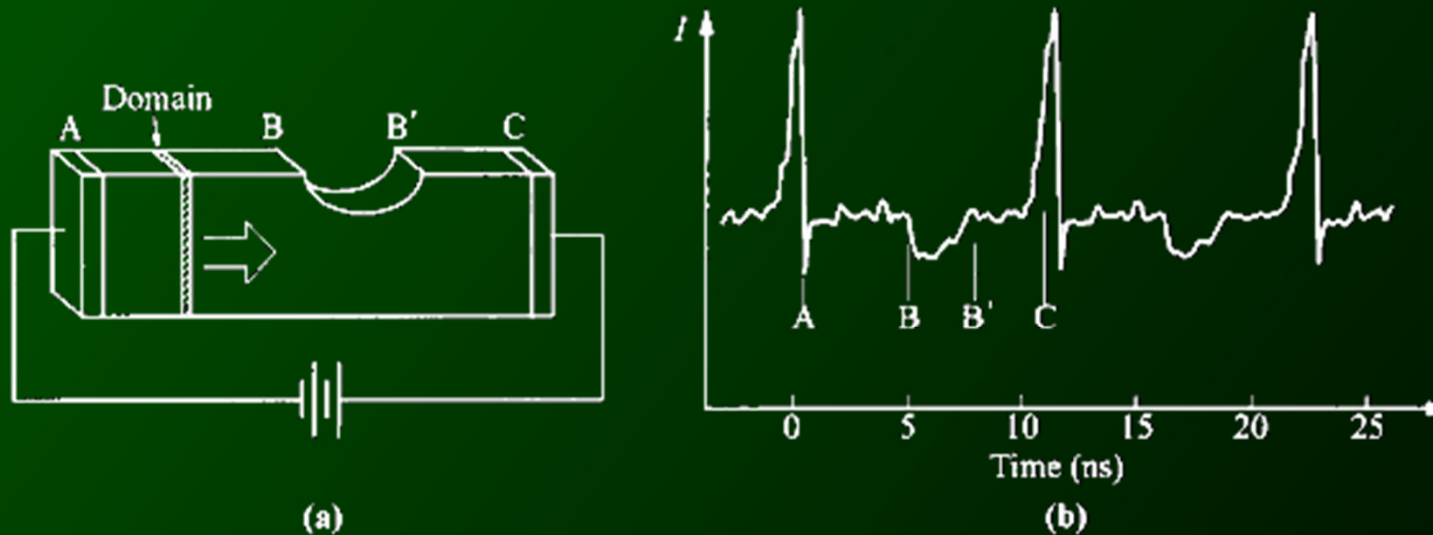
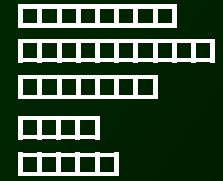


Fig. 18 (a) A TED with nonuniform cross-section and (b) its current waveform. Labels in (b) correspond to the location of domain at the specific time. (After Ref. 47.)

limited space-charge accumulation (LSA) mode

Negative Differential Resistance Transistor

1. I
- 2.
- 3.
- 4.
- 5.

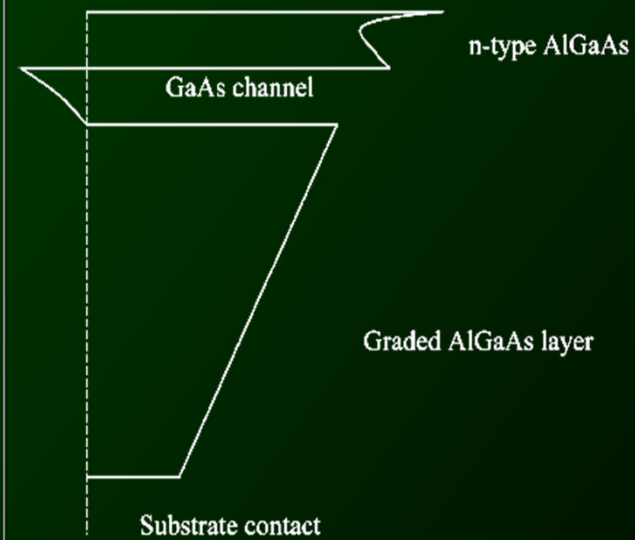
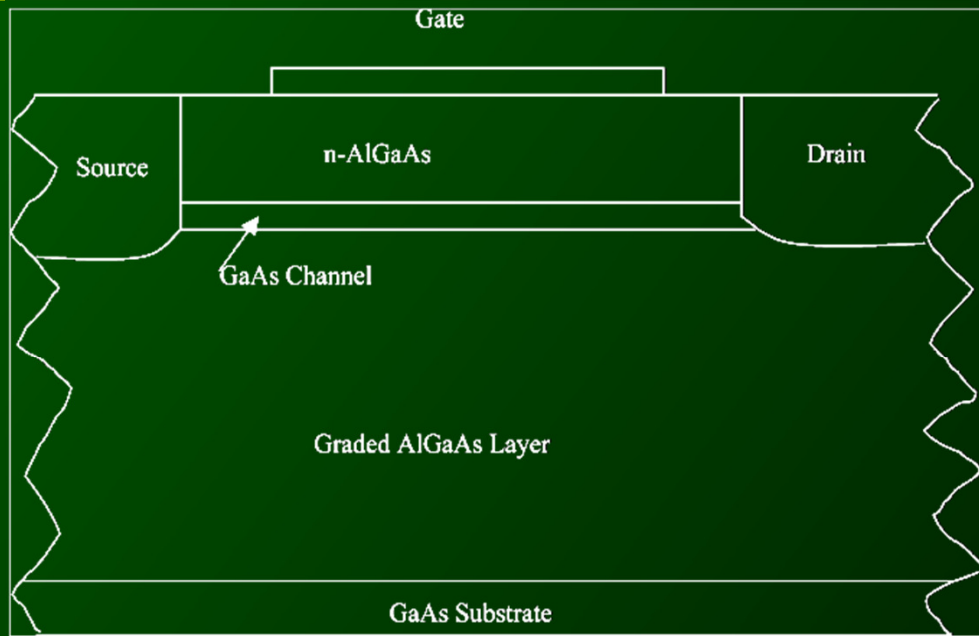
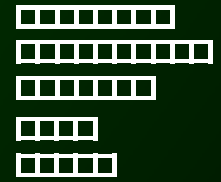
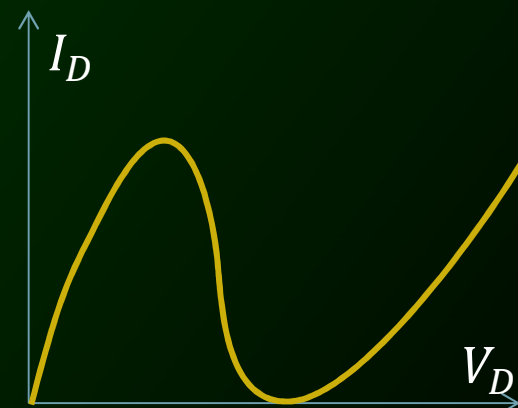


FIGURE 5.6.1 Real-space transfer field-effect transistor (NERFET).



IMPATT Diode

1. I	□□□□□□
2.	□□□□□□□□
3.	□□□□□□
4.	□□□□
5.	□□□□

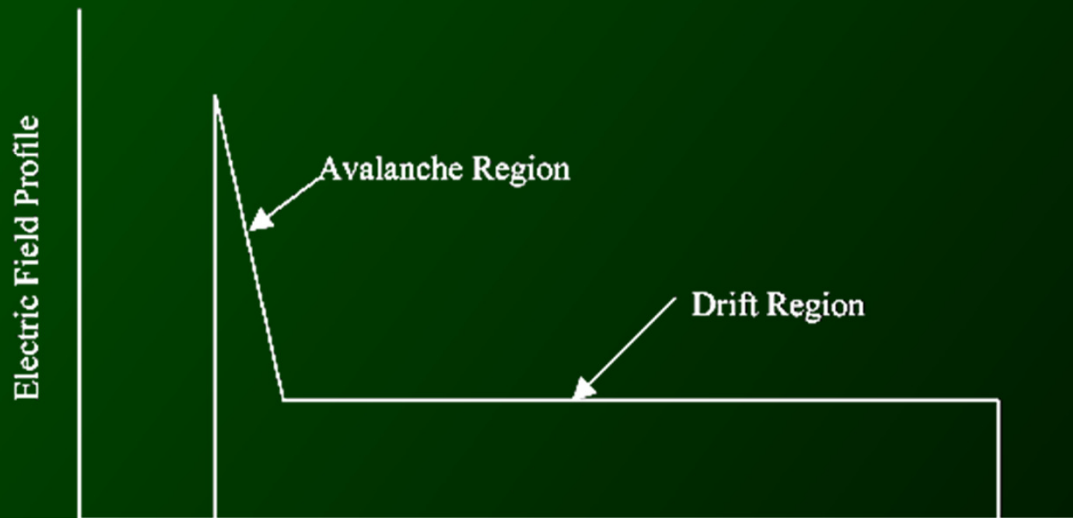
IMPATT diode is not generally a transferred electron device

Within the IMPATT NDR is induced by driving the current and voltage out of phase with one another



(a)

avalanche and transit time regions must be designed such that the current and voltage are driven out of phase



(b)

- (1) the finite rise and decay time of the avalanche current
- (2) the finite transit time of the carriers through the drift region

HBT

1. I
- 2.
- 3.
- 4.
- 5.

