LM723/LM723C Voltage Regulator

Check for Samples: LM723, LM723C

FEATURES
- 150 mA Output Current Without External Pass Transistor
- Output Currents in Excess of 10A Possible by Adding External Transistors
- Input Voltage 40V Max
- Output Voltage Adjustable from 2V to 37V
- Can be Used as Either a Linear or a Switching Regulator

DESCRIPTION
The LM723/LM723C is a voltage regulator designed primarily for series regulator applications. By itself, it will supply output currents up to 150 mA; but external transistors can be added to provide any desired load current. The circuit features extremely low standby current drain, and provision is made for either linear or foldback current limiting.

The LM723/LM723C is also useful in a wide range of other applications such as a shunt regulator, a current regulator or a temperature controller.

The LM723C is identical to the LM723 except that the LM723C has its performance ensured over a 0 °C to +70 °C temperature range, instead of −55 °C to +125 °C.

Note: Pin 5 connected to case.

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Equivalent Circuit*

*Pin numbers refer to metal can package.

Typical Application

Typical Performance

Regulated Output Voltage 5V
Line Regulation ($\Delta V_{IN} = 3V$) 0.5mV
Load Regulation ($\Delta I_L = 50 mA$) 1.5mV

Figure 4. Basic Low Voltage Regulator ($V_{OUT} = 2$ to $7$ Volts)
These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

**ABSOLUTE MAXIMUM RATINGS**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>LM723</th>
<th>LM723C</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Pulse Voltage from V+ to V− (50 ms)</td>
<td>50V</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Continuous Voltage from V+ to V−</td>
<td>40V</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input-Output Voltage Differential</td>
<td>40V</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Maximum Amplifier Input Voltage (Either Input)</td>
<td>8.5V</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Maximum Amplifier Input Voltage (Differential)</td>
<td>5V</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Current from VZ</td>
<td>25 mA</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Current from VREF</td>
<td>15 mA</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Internal Power Dissipation Metal Can</td>
<td>800 mW</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CDIP</td>
<td>900 mW</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>PDIP</td>
<td>660 mW</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**Operating Temperature Range**

- LM723: −55°C to +150°C
- LM723C: 0°C to +70°C

**Storage Temperature Range**

- Metal Can: −65°C to +150°C
- PDIP: −55°C to +150°C

**Lead Temperature (Soldering, 4 sec. max.)**

- Hermetic Package: 300°C
- Plastic Package: 260°C
- ESD Tolerance: 1200V

(1) “Absolute Maximum Ratings” indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits.

(2) A military RETS specification is available on request. At the time of printing, the LM723 RETS specification complied with the Min and Max limits in this table. The LM723E, H, and J may also be procured as a Standard Military Drawing.

(3) See derating curves for maximum power rating above 25°C.

**ELECTRICAL CHARACTERISTICS**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>LM723</th>
<th>LM723C</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Line Regulation</td>
<td>VIN = 12V to VIN = 15V</td>
<td>0.01</td>
<td>0.1</td>
<td>0.01</td>
</tr>
<tr>
<td></td>
<td>−55°C ≤ TA ≤ +125°C</td>
<td>0.3</td>
<td>0.1</td>
<td>0.3</td>
</tr>
<tr>
<td></td>
<td>0°C ≤ TA ≤ +70°C</td>
<td>0.02</td>
<td>0.2</td>
<td>0.1</td>
</tr>
<tr>
<td></td>
<td>VIN = 12V to VIN = 40V</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Load Regulation</td>
<td>IL = 1 mA to IL = 50 mA</td>
<td>0.03</td>
<td>0.15</td>
<td>0.03</td>
</tr>
<tr>
<td></td>
<td>−55°C ≤ TA ≤ +125°C</td>
<td>0.15</td>
<td>0.6</td>
<td>0.2</td>
</tr>
<tr>
<td></td>
<td>0°C ≤ TA ≤ +70°C</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Ripple Rejection</td>
<td>f = 50 Hz to 10 kHz, CREF = 0</td>
<td>74</td>
<td>74</td>
<td>dB</td>
</tr>
<tr>
<td></td>
<td>f = 50 Hz to 10 kHz, CREF = 5 μF</td>
<td>86</td>
<td>86</td>
<td>dB</td>
</tr>
</tbody>
</table>

(1) Unless otherwise specified, TA = 25°C, VIN = V+ = V− = 12V, VOUT = 5V, IL = 1 mA, RSC = 0, C = 100 pF, CREF = 0 and divider impedance as seen by error amplifier ≤ 10 kΩ connected as shown in Figure 4. Line and load regulation specifications are given for the condition of constant chip temperature. Temperature drifts must be taken into account separately for high dissipation conditions.

(2) A military RETS specification is available on request. At the time of printing, the LM723 RETS specification complied with the Min and Max limits in this table. The LM723E, H, and J may also be procured as a Standard Military Drawing.

(3) Specified by correlation to other tests.

(4) L1 is 40 turns of No. 20 enameled copper wire wound on Ferroxcube P36/22-387 pot core or equivalent with 0.009 in. air gap.
### ELECTRICAL CHARACTERISTICS (1)(2)(3)(4)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>LM723</th>
<th>LM723C</th>
<th>Units</th>
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<tr>
<td></td>
<td></td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Min</td>
<td>Typ</td>
<td>Max</td>
</tr>
<tr>
<td>Average Temperature Coefficient of Output Voltage (°C)</td>
<td>-55°C ≤ T&lt;sub&gt;A&lt;/sub&gt; ≤ +125°C</td>
<td>0.002</td>
<td>0.015</td>
<td></td>
</tr>
<tr>
<td></td>
<td>0°C ≤ T&lt;sub&gt;A&lt;/sub&gt; ≤ +70°C</td>
<td>0.003</td>
<td>0.015</td>
<td></td>
</tr>
<tr>
<td>Short Circuit Current Limit</td>
<td>R&lt;sub&gt;SC&lt;/sub&gt; = 10Ω, V&lt;sub&gt;OUT&lt;/sub&gt; = 0</td>
<td>65</td>
<td>65</td>
<td></td>
</tr>
<tr>
<td>Reference Voltage</td>
<td></td>
<td>6.95</td>
<td>7.15</td>
<td>7.35</td>
</tr>
<tr>
<td>Output Noise Voltage</td>
<td>BW = 100 Hz to 10 kHz, C&lt;sub&gt;REF&lt;/sub&gt; = 0</td>
<td>86</td>
<td>86</td>
<td></td>
</tr>
<tr>
<td></td>
<td>BW = 100 Hz to 10 kHz, C&lt;sub&gt;REF&lt;/sub&gt; = 5 μF</td>
<td>2.5</td>
<td>2.5</td>
<td></td>
</tr>
<tr>
<td>Long Term Stability</td>
<td></td>
<td>0.05</td>
<td>0.05</td>
<td></td>
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<tr>
<td>Standby Current Drain</td>
<td>I&lt;sub&gt;L&lt;/sub&gt; = 0, V&lt;sub&gt;IN&lt;/sub&gt; = 30V</td>
<td>1.7</td>
<td>3.5</td>
<td></td>
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<tr>
<td>Input Voltage Range</td>
<td></td>
<td>9.5</td>
<td>40</td>
<td>9.5</td>
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<tr>
<td>Output Voltage Range</td>
<td></td>
<td>2.0</td>
<td>37</td>
<td>2.0</td>
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<tr>
<td>Input-Output Voltage Differential</td>
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<td>3.0</td>
<td>38</td>
<td>3.0</td>
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<tr>
<td>θ&lt;sub&gt;JA&lt;/sub&gt;</td>
<td>PDIP</td>
<td></td>
<td></td>
<td>105</td>
</tr>
<tr>
<td>θ&lt;sub&gt;JA&lt;/sub&gt;</td>
<td>CDIP</td>
<td></td>
<td></td>
<td>150</td>
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<tr>
<td>θ&lt;sub&gt;JA&lt;/sub&gt;</td>
<td>H10C Board Mount in Still Air</td>
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<td></td>
<td>165</td>
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<tr>
<td>θ&lt;sub&gt;JA&lt;/sub&gt;</td>
<td>H10C Board Mount in 400 LF/Min Air Flow</td>
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<td></td>
<td>66</td>
</tr>
<tr>
<td>θ&lt;sub&gt;JC&lt;/sub&gt;</td>
<td></td>
<td></td>
<td></td>
<td>22</td>
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</tbody>
</table>

(5) For metal can applications where V<sub>z</sub> is required, an external 6.2V zener diode should be connected in series with V<sub>OUT</sub>-
TYPICAL PERFORMANCE CHARACTERISTICS

Load Regulation Characteristics with Current Limiting

Load & Line Regulation vs Input-Output Voltage Differential

Current Limiting Characteristics vs Junction Temperature

Standby Current Drain vs Input Voltage

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Product Folder Links: LM723 LM723C
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

Line Transient Response

Figure 11.

Load Transient Response

Figure 12.

Output Impedance vs Frequency

Figure 13.
MAXIMUM POWER RATINGS

Noise vs Filter Capacitor
(CREF in Circuit of Figure 4)
(Bandwidth 100 Hz to 10 kHz)

Figure 14.

Power Dissipation vs Ambient Temperature

Figure 15.

Power Dissipation vs Ambient Temperature

Figure 16.
### Table 1. Resistor Values (kΩ) for Standard Output Voltage

<table>
<thead>
<tr>
<th>Positive Output Voltage</th>
<th>Applicable Figures</th>
<th>Fixed Output ±5%</th>
<th>Output Adjustable ±10%</th>
<th>Output ±5%</th>
<th>Applicable Figures</th>
<th>Fixed Output ±5%</th>
<th>5% Output Adjustable ±10%</th>
<th>R1</th>
<th>R2</th>
<th>R1</th>
<th>P1</th>
<th>R2</th>
<th>R1</th>
<th>R2</th>
<th>R1</th>
<th>P1</th>
<th>R2</th>
</tr>
</thead>
<tbody>
<tr>
<td>+3.0</td>
<td>Figure 4, Figure 19, Figure 21, Figure 24, Figure 27 (Figure 19)</td>
<td>4.12 3.01</td>
<td>1.8 0.5 1.2</td>
<td>+100</td>
<td>Figure 22</td>
<td>3.57 102</td>
<td>2.2 10</td>
<td>91</td>
<td></td>
<td></td>
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<tr>
<td>+3.6</td>
<td>Figure 4, Figure 19, Figure 21, Figure 24, Figure 27 (Figure 19)</td>
<td>3.57 3.65</td>
<td>1.5 0.5 1.5</td>
<td>+250</td>
<td>Figure 22</td>
<td>3.57 255</td>
<td>2.2 10</td>
<td>240</td>
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<tr>
<td>+5.0</td>
<td>Figure 4, Figure 19, Figure 21, Figure 24, Figure 27 (Figure 19)</td>
<td>2.15 4.99</td>
<td>0.75 0.5 2.2</td>
<td>−6</td>
<td>Figure 18, (Figure 25)</td>
<td>3.57 2.43</td>
<td>1.2 0.5</td>
<td>0.75</td>
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<tr>
<td>+6.0</td>
<td>Figure 4, Figure 19, Figure 21, Figure 24, Figure 27 (Figure 19)</td>
<td>1.15 6.04</td>
<td>0.5 0.5 2.7</td>
<td>−9</td>
<td>Figure 18, Figure 25</td>
<td>3.48 5.36</td>
<td>1.2 0.5</td>
<td>2.0</td>
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<tr>
<td>+9.0</td>
<td>Figure 17, Figure 19, Figure 19, Figure 21, Figure 24, Figure 27</td>
<td>1.87 7.15</td>
<td>0.75 1.0 2.7</td>
<td>−12</td>
<td>Figure 18, Figure 25</td>
<td>3.57 8.45</td>
<td>1.2 0.5</td>
<td>3.3</td>
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<tr>
<td>+12</td>
<td>Figure 17, Figure 19, Figure 19, Figure 21, Figure 24, Figure 27</td>
<td>4.87 7.15</td>
<td>2.0 1.0 3.0</td>
<td>−15</td>
<td>Figure 18, Figure 25</td>
<td>3.65 11.5</td>
<td>1.2 0.5</td>
<td>4.3</td>
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<tr>
<td>+15</td>
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<td>7.87 7.15</td>
<td>3.3 1.0 3.0</td>
<td>−28</td>
<td>Figure 18, Figure 25</td>
<td>3.57 24.3</td>
<td>1.2 0.5</td>
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</tr>
<tr>
<td>+28</td>
<td>Figure 17, Figure 19, Figure 19, Figure 21, Figure 24, Figure 27</td>
<td>21.0 7.15</td>
<td>5.6 1.0 2.0</td>
<td>−45</td>
<td>Figure 23</td>
<td>3.57 41.2</td>
<td>2.2 10</td>
<td>33</td>
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<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>+45</td>
<td>Figure 22</td>
<td>3.57 48.7</td>
<td>2.2 10 39</td>
<td>−100</td>
<td>Figure 23</td>
<td>3.57 97.6</td>
<td>2.2 10</td>
<td>91</td>
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<td></td>
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<td></td>
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<tr>
<td>+75</td>
<td>Figure 22</td>
<td>3.57 78.7</td>
<td>2.2 10 68</td>
<td>−250</td>
<td>Figure 23</td>
<td>3.57 249</td>
<td>2.2 10</td>
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<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

(1) Replace R1/R2 in figures with divider shown in Figure 28.
(2) Figures in parentheses may be used if R1/R2 divider is placed on opposite input of error amp.
(3) V⁺ and VCC must be connected to a +3V or greater supply.

### Table 2. Formulae for Intermediate Output Voltages

<table>
<thead>
<tr>
<th>Outputs from +2 to +7 volts</th>
<th>Outputs from +4 to +250 volts</th>
<th>Current Limiting</th>
</tr>
</thead>
<tbody>
<tr>
<td>(Figure 4 Figure 19 Figure 20 Figure 21 Figure 24 Figure 27)</td>
<td>(Figure 22)</td>
<td>( I_{\text{LIMIT}} = \frac{V_{\text{SENSE}}}{R_{\text{SC}}} )</td>
</tr>
<tr>
<td>( V_{\text{OUT}} = \left( V_{\text{REF}} \times \frac{R_2}{R_1 + R_2} \right) )</td>
<td>( V_{\text{OUT}} = \left( V_{\text{REF}} \times \frac{R_2 - R_1}{R_1} \right) )</td>
<td>( R_3 = R_4 )</td>
</tr>
<tr>
<td>Outputs from +7 to +37 volts</td>
<td>Outputs from −6 to −250 volts</td>
<td>Foldback Current Limiting</td>
</tr>
<tr>
<td>(Figure 17 Figure 19 Figure 20 Figure 21 Figure 24 Figure 27)</td>
<td>(Figure 18 Figure 23 Figure 25)</td>
<td>( I_{\text{KNEE}} = \frac{V_{\text{OUT}} R_3}{R_{\text{SC}} R_4} + \frac{V_{\text{SENSE}} (R_3 + R_4)}{R_{\text{SC}} R_4} )</td>
</tr>
<tr>
<td>( V_{\text{OUT}} = \left( V_{\text{REF}} \times \frac{R_1 + R_2}{R_2} \right) )</td>
<td>( V_{\text{OUT}} = \left( V_{\text{REF}} \times \frac{R_1 + R_2}{R_1} \right) )</td>
<td>( I_{\text{SHORT}} = \left( \frac{V_{\text{SENSE}}}{R_{\text{SC}}} \right) \times \left( \frac{R_3 + R_4}{R_4} \right) )</td>
</tr>
</tbody>
</table>
TYPICAL APPLICATIONS

![Circuit Diagram]

Note: $R_3 = \frac{R_1 \cdot R_2}{R_1 + R_2}$ for minimum temperature drift.
$R_3$ may be eliminated for minimum component count.

### Typical Performance

<table>
<thead>
<tr>
<th></th>
<th>Regulated Output Voltage</th>
<th>15V</th>
</tr>
</thead>
<tbody>
<tr>
<td>Line Regulation ($\Delta V_{IN} = 3V$)</td>
<td>1.5 mV</td>
<td></td>
</tr>
<tr>
<td>Load Regulation ($\Delta I_L = 50 mA$)</td>
<td>4.5 mV</td>
<td></td>
</tr>
</tbody>
</table>

**Figure 17. Basic High Voltage Regulator ($V_{OUT} = 7$ to 37 Volts)**

<table>
<thead>
<tr>
<th></th>
<th>Regulated Output Voltage</th>
<th>$-15V$</th>
</tr>
</thead>
<tbody>
<tr>
<td>Line Regulation ($\Delta V_{IN} = 3V$)</td>
<td>1 mV</td>
<td></td>
</tr>
<tr>
<td>Load Regulation ($\Delta I_L = 100 mA$)</td>
<td>2 mV</td>
<td></td>
</tr>
</tbody>
</table>

**Figure 18. Negative Voltage Regulator**
Typical Performance

Regulated Output Voltage +15V
Line Regulation ($\Delta V_{IN} = 3\text{V}$) 1.5 mV
Load Regulation ($\Delta I_L = 1\text{A}$) 15 mV

Figure 19. Positive Voltage Regulator (External NPN Pass Transistor)

Typical Performance

Regulated Output Voltage +5V
Line Regulation ($\Delta V_{IN} = 3\text{V}$) 0.5 mV
Load Regulation ($\Delta I_L = 1\text{A}$) 5 mV

Figure 20. Positive Voltage Regulator (External PNP Pass Transistor)
Typical Performance

Regulated Output Voltage
Line Regulation ($\Delta V_{IN} = 3V$) 0.5 mV
Load Regulation ($\Delta I_L = 10 mA$) 1 mV
Short Circuit Current 20 mA

Figure 21. Foldback Current Limiting

Regulated Output Voltage $+5V$
Line Regulation ($\Delta V_{IN} = 20V$) 15 mV
Load Regulation ($\Delta I_L = 50 mA$) 20 mV

Figure 22. Positive Floating Regulator
Typical Performance

Regulated Output Voltage
-100V

Line Regulation ($\Delta V_{IN} = 20\text{V}$)
30 mV

Load Regulation ($\Delta I_L = 100\text{ mA}$)
20 mV

Figure 23. Negative Floating Regulator

Typical Performance

Regulated Output Voltage
+5V

Line Regulation ($\Delta V_{IN} = 30\text{V}$)
10 mV

Load Regulation ($\Delta I_L = 2\text{A}$)
80 mV

Figure 24. Positive Switching Regulator
Typical Performance

Regulated Output Voltage
Line Regulation \( (\Delta V_{IN} = 20V) \)
Load Regulation \( (\Delta I_L = 2A) \)

\[-15V \quad 8 \text{ mV} \quad 6 \text{ mV} \]

**Figure 25. Negative Switching Regulator**

Note: Current limit transistor may be used for shutdown if current limiting is not required.

Typical Performance

Regulated Output Voltage
Line Regulation \( (\Delta V_{IN} = 3V) \)
Load Regulation \( (\Delta I_L = 50 \text{ mA}) \)

\[+5V \quad 0.5 \text{ mV} \quad 1.5 \text{ mV} \]

**Figure 26. Remote Shutdown Regulator with Current Limiting**
Regulated Output Voltage
+5V
Line Regulation ($\Delta V_{IN} = 10V$) 0.5 mV
Load Regulation ($\Delta I_L = 100 \text{ mA}$) 1.5 mV

Figure 27. Shunt Regulator

Figure 28. Output Voltage Adjust (1)

(1) Replace R1/R2 in figures with divider shown in Figure 28.
## REVISION HISTORY

**Changes from Revision B (April 2013) to Revision C**

- Changed layout of National Data Sheet to TI format .......................... 15
## PACKAGING INFORMATION

<table>
<thead>
<tr>
<th>Orderable Device</th>
<th>Status (1)</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>Package Qty</th>
<th>Eco Plan (2)</th>
<th>Lead/Ball Finish</th>
<th>MSL Peak Temp (3)</th>
<th>Op Temp (°C)</th>
<th>Device Marking (4/5)</th>
<th>Samples</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM723CH</td>
<td>ACTIVE</td>
<td>TO-100</td>
<td>LME</td>
<td>10</td>
<td>500</td>
<td>TBD</td>
<td>Call TI</td>
<td>Call TI</td>
<td>0 to 70</td>
<td>LM723CH</td>
<td>Samples</td>
</tr>
<tr>
<td>LM723CH/NOPB</td>
<td>ACTIVE</td>
<td>TO-100</td>
<td>LME</td>
<td>10</td>
<td>500</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>POST-PLATE</td>
<td>Level-1-NA-UNLIM</td>
<td>0 to 70</td>
<td>LM723CH</td>
<td>Samples</td>
</tr>
<tr>
<td>LM723CN</td>
<td>NRND</td>
<td>PDIP</td>
<td>NFF</td>
<td>14</td>
<td>25</td>
<td>TBD</td>
<td>Call TI</td>
<td>Call TI</td>
<td>0 to 70</td>
<td>LM723CN</td>
<td>Samples</td>
</tr>
<tr>
<td>LM723CN/NOPB</td>
<td>ACTIVE</td>
<td>PDIP</td>
<td>NFF</td>
<td>14</td>
<td>25</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>CU SN</td>
<td>Level-1-NA-UNLIM</td>
<td>0 to 70</td>
<td>LM723CN</td>
<td>Samples</td>
</tr>
<tr>
<td>LM723H</td>
<td>ACTIVE</td>
<td>TO-100</td>
<td>LME</td>
<td>10</td>
<td>500</td>
<td>TBD</td>
<td>Call TI</td>
<td>Call TI</td>
<td>-55 to 150</td>
<td>LM723H</td>
<td>Samples</td>
</tr>
<tr>
<td>LM723H/NOPB</td>
<td>ACTIVE</td>
<td>TO-100</td>
<td>LME</td>
<td>10</td>
<td>500</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>POST-PLATE</td>
<td>Level-1-NA-UNLIM</td>
<td>-55 to 150</td>
<td>LM723H</td>
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<td>-55 to 150</td>
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<tr>
<td>U5R7723393</td>
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<td>TBD</td>
<td>Call TI</td>
<td>Call TI</td>
<td>0 to 70</td>
<td>LM723CH</td>
<td>Samples</td>
</tr>
</tbody>
</table>

(1) The marketing status values are defined as follows:
- **ACTIVE**: Product device recommended for new designs.
- **LIFEBUY**: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
- **NRND**: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
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(2) Eco Plan - The planned eco-friendly classification; Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check [http://www.ti.com/productcontent](http://www.ti.com/productcontent) for the latest availability information and additional product content details.

- **TBD**: The Pb-Free/Green conversion plan has not been defined.

- **Pb-Free (RoHS)**: TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

- **Pb-Free (RoHS Exempt)**: This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

- **Green (RoHS & no Sb/Br)**: TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) **MSL, Peak Temp.** - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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LME (O–MBCY–W10) METAL CYLINDRICAL PACKAGE

NOTES:
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B. This drawing is subject to change without notice.
C. Leads in true position within 0.010 (0,25) R @ MMC at seating plane.
D. Pin numbers shown for reference only. Numbers may not be marked on package.
E. Falls within JEDEC MO–006/TO-100.
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