

Sayyed Saeed Jazaeri

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EDUCATION

Sharif University of Technology

M.Sc. Electrical Engineering
Digital Systems

Tehran, Iran

Sept 2024 – Present
GPA: 19.63/20 (till now)

Sharif University of Technology

B.Sc. Electrical Engineering
Digital Systems

Tehran, Iran

Sept 2019 – June 2024
GPA: 17.62/20

Thesis: "Implementation of 3D Image Reconstruction Algorithm for MMW Imaging on FPGA"

Sadat High School

Mathematics and Physics, Diploma

Isfahan, Iran

Sept 2013 – June 2019
GPA: 19.58/20

RELEVANT COURSES

Course Name	Grade	Course Name	Grade
Digital VLSI Design [†]	20/20	Computer Interface Circuits [†]	20/20
Massive Data Analysis [†]	20/20	Parallel Processing [†]	19.5/20
FPGA/ASIC System Design	20/20	Embedded Real-Time Systems	19.6/20
Microprocessor Systems Design	19.5/20	Digital Communication	19.5/20
Sensing and Measurement	19.2/20	Linear Algebra	19.2/20
Artificial Intelligence	18.8/20	Computer Network	17/20

[†] Graduate-level course

FIELDS OF INTERESTS

- FPGA Design and Hardware Acceleration
- Computer Architecture and Microprocessor
- Digital IC Design
- Digital Electronics and VLSI
- Embedded Systems

TEACHING EXPERIENCE

Computer Architecture and Lab

- Laboratory Assistant

Prof. Hajsadeghi

Object Oriented Programming(JAVA)

- Quiz Team

Prof. Matin Hashemi

Electrical Energy Conversion

- MATLAB Simulink Assistant (Twice)

Prof. Nasiri Gheydari

WORK EXPERIENCE And PROJECTS

Tarashe Fanavar Maad

Digital ASIC Designer

Tehran, Iran

June 2024– Present

- Digital ASIC Designer
 - Design Digital ASIC circuits - Front End.
 - Verification Of Digital Design.
 - Embedded System and Microcontroller Designer.

Basir Wave Technologies

FPGA Engineer and Hardware Designer

Tehran, Iran

Jul 2022– June 2024

- FPGA Designer
 - Developed and implemented processing and controller modules.

National Elite Foundation

IoT Platform In Smart Home With The Focus On Security Cameras

Tehran, Iran

Jan 2022– Sep 2022

- Engineered an Android TV application for the Allwinner H616 Dev Board to support first-stage processing and edge communication.
 - Developed and implemented user authentication features, including login, logout, and registration functionalities.

SKILLS & KNOWLEDGE AREAS

- **Programming Languages:** C, C++, Java, Python, MATLAB, C#, Verilog. SystemVerilog, Perl, TCL
- **Simulation:** Spice, Simulink(Basic), Proteus, Modelsim. VCS
- **Softwares and Development Tools:** Xilinx Vivado, Altera Quartus, Synopsys DesignCompiler, Cadence Innovus, HLS-SDK, Android Studio, VSCode, Keil, Cube IDE.
- **Hardware and Embedded Systems:** ST microcontrollers, Zynq FPGA, ARM Architecture, RaspberryPI Pico.
- **Others:** L^AT_EX, Git, Jira, Linux, Gitlab, Github.
- **Languages:** Persian(Native), English.

HONORS AND REWARDS

- 🏆 Ranked 6th among many talented students in the 29th Iran National Olympiad in Electrical Engineering, 2024.
- 🏆 Ranked 6th among more than 9000 students taking part in Iran National University Entrance Exam For M.Sc degree (Konkour Arshad) in Electrical Electronics, 2024.
- 🏆 Selected as **Outstanding B.Sc. Thesis Award** in the IEEE Iran Section presents in the 2025